

A Study of Recent Contribution on Simulation Tools for Network-on-Chip

Authors : Muthana Saleh Alalaki, Michael Opoku Agyeman

Abstract : The growth in the number of Intellectual Properties (IPs) or the number of cores on the same chip becomes a critical issue in System-on-Chip (SoC) due to the intra-communication problem between the chip elements. As a result, Network-on-Chip (NoC) has emerged as a system architecture to overcome intra-communication issues. This paper presents a study of recent contributions on simulation tools for NoC. Furthermore, an overview of NoC is covered as well as a comparison between some NoC simulators to help facilitate research in on-chip communication.

Keywords : WiNoC, simulation tool, network-on-chip, SoC

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