

An Application of Graph Theory to The Electrical Circuit Using Matrix Method

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Abstract : A graph is a pair of two set and so that a graph is a pictorial representation of a system using two basic element nodes and edges. A node is represented by a circle (either halo shade) and edge is represented by a line segment connecting two nodes together. In this paper, we present a circuit network in the concept of graph theory application and also circuit models of graph are represented in logical connection method were we formulate matrix method of adjacency and incidence of matrix and application of truth table.

Keywords : euler circuit and path, graph representation of circuit networks, representation of graph models, representation of circuit network using logical truth table

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