

## Efficient Study of Substrate Integrated Waveguide Devices

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**Abstract :** This paper presents a study of SIW circuits (Substrate Integrated Waveguide) with a rigorous and fast original approach based on Iterative process (WCIP). The theoretical suggested study is validated by the simulation of two different examples of SIW circuits. The obtained results are in good agreement with those of measurement and with software HFSS.

**Keywords :** convergence study, HFSS, modal decomposition, SIW circuits, WCIP method

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