

Simulation of Hamming Coding and Decoding for Microcontroller Radiation Hardening

Authors : Rehab I. Abdul Rahman, Mazhar B. Tayel

Abstract : This paper presents a method of hardening the 8051 microcontroller, that able to assure reliable operation in the presence of bit flips caused by radiation. Aiming at avoiding such faults in the 8051 microcontroller, Hamming code protection was used in its SRAM memory and registers. A VHDL code and its simulation have been used for this hamming code protection.

Keywords : radiation, hardening, bitflip, hamming

Conference Title : ICECECE 2015 : International Conference on Electrical, Computer, Electronics and Communication Engineering

Conference Location : Paris, France

Conference Dates : February 23-24, 2015