Abstract—In this paper, we present a comparative assessment of Space Vector Pulse Width Modulation (SVPWM) and Model Predictive Control (MPC) for two-level three phase (2L-3P) Voltage Source Inverter (VSI). VSI with associated system is subjected to both control techniques and the results are compared. Matlab/Simulink was used to model, simulate and validate the control schemes. Findings of this study show that MPC is superior to SVPWM in terms of total harmonic distortion (THD) and implementation.

Keywords—Model Predictive Control, Space Vector Pulse Width Modulation, Voltage Source Inverter.

I. INTRODUCTION

The DC to AC power converters are known as inverters. Inverters are mainly classified as current and voltage source inverters. Current source inverters have DC current source at input and use switches to adjust the output current and frequency. On the hand, voltage source inverters (VSI) convert DC power into AC power at desired voltage and frequency [1]. The AC output voltage and frequency may be fixed or variable depending on the application [2].

Semiconductor switches, such as Bipolar Junction Transistors, Metal Oxide Semi-conductor Field Effect Transistors, Insulated Gate Bipolar Transistors, etc., are used to control and adjust the output parameters [3]. Anti-parallel diodes are connected across from the switches to add a reverse current capability [2]. VSI requires a stiff DC voltage source at input [1] for quality operation. The input source could be the output of a rectifier, battery, fuel cell, or photo voltaic (PV) system.

Fig. 1 shows a basic two-level three phase (2L-3P) VSI circuit, where P is positive, N is negative terminal and O is a null point. VSI has three legs each with two switches and two anti-parallel diodes. For example, leg-1 has switches 1 (S1) and 4 (S4) and diodes 1 (D1) and 4 (D4). To prevent short circuit across terminals switches of the same leg should never be turned on simultaneously. Therefore, intentional switching delay (known as dead or blanking time) is introduced to ensure the first switch is turned off before the complementary switch of the same leg is turned on. If a short circuit happens, the fault current rises very quickly and unless it is controlled by proper protecting measures, it can damage the system. Similarly, switches of the same leg are never turned off simultaneously to avoid uncertain output voltage.

Though the output voltage can be controlled by adjusting $V_{DC}$, usually the more convenient method of Pulse Width Modulation (PWM) is used. PWM adjusts pulse width by increasing or decreasing on and off times of the switches to control the output voltage. Conventionally, the on and off commands are issued by comparing three phase modulating signals $(V_{mA}, V_{mB}, V_{mC})$ with a carrier wave $(V_C)$ (1).

$$V_{AN} = \begin{cases} V_{DC} & \text{if } V_{mA} > V_C \\ 0 & \text{if } V_{mA} < V_C \end{cases}$$

where $x=A,B,C$

For example, if $V_{mA} > V_C$, $V_{AN} = V_{DC}$ with $S_1$ on and $S_4$ off. On the other hand, if $V_{mA} < V_C$, $V_{AN} = 0$ with $S_1$ off and $S_4$ on. The status of switches for all three legs is shown in Table I.

<table>
<thead>
<tr>
<th>TABLE I THE STATUS OF SWITCHES IN INDIVIDUAL LEGS</th>
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<tr>
<td>S States</td>
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<tr>
<td>S1</td>
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<tr>
<td>P</td>
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<tr>
<td>O</td>
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Since the input lines should never be shortened and the output current should always be continuous, a voltage source inverter can assume only 8 distinct switching states, defined by (2) and presented in Table II.

$$\text{States} = n^3$$

where $n=2$ for two level VSI.

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Phase load voltages and line to line voltages.

\[ V_{AB} = V_{AN} - V_{BN} \]  

Multiple methods exist for controlling VSI. This paper compares Space Vector Pulse Width Modulation (SVPWM) with Model Predictive Control (MPC) for VSI. The rest of the paper is organized into the following sections: (II) DQ transformation, (III) SVPWM, (IV) MPC, (V) System Performance Assessment, (VI) Results and Discussion, (VII) Conclusions.

\[ X_{ao} + X_{bo} + X_{co} = 0 \]  

Table III shows the phase load and line to line voltages.

Converting the three phase control problem into two phase control problem makes analysis, design and control of the VSI and the associated system simple. Although \( a\beta \) conversion reduces one phase and therefore simplifies the system control, the two dimensional frame remains sinusoidal. On the other hand \( dq \) synchronous not only results in two phase but a DC control problem, enabling use of PID controllers. \( dq \) transformation in any reference frame can be obtained by (5) and (6). \( \theta \) is used to define the reference frame: the reference frame is Stationary \( (a\beta) \) if \( \theta = 0 \), Synchronous if \( \theta = \theta_{grid} \) and Arbitrary if \( \theta \) is neither zero nor equal to \( \theta_{grid} \). The \( a\beta \) reference frame is used to control VSI in both SVPWM and
MPC.

Equation (5) gives the transformation from ABC to dq (direct), while (6) gives the transformation from dq to ABC (inverse).

\[
\begin{bmatrix}
X_t \\
X_q
\end{bmatrix} = \frac{2}{3} \begin{bmatrix}
\cos \beta \cos (\theta - \frac{2\pi}{3}) & \cos \beta \cos (\theta - \frac{\pi}{3}) \\
-\sin \beta \sin (\theta - \frac{2\pi}{3}) & -\sin \beta \sin (\theta - \frac{\pi}{3})
\end{bmatrix} \begin{bmatrix}
X_d \\
X_q
\end{bmatrix}
\]

(5)

\[
\begin{bmatrix}
X_q \\
X_d
\end{bmatrix} = \begin{bmatrix}
\cos \beta \cos (\theta + \frac{\pi}{3}) & -\sin \beta \sin (\theta + \frac{\pi}{3}) \\
\cos \beta \sin (\theta + \frac{\pi}{3}) & \sin \beta \cos (\theta + \frac{\pi}{3})
\end{bmatrix} \begin{bmatrix}
X_d \\
X_q
\end{bmatrix}
\]

(6)

where X=V, I, etc.

III. SVPWM

Sinusoidal PWM (SPWM), a very popular technique, creates a relatively high harmonic distortion due to its asymmetrical nature of the PWM switching and cannot make use of the inverter’s supply voltage fully [3]. SVPWM however, provides a higher voltage and lower total harmonic distortion (THD) and is therefore, preferred.

SVPWM control strategy is based on the fact that the VSI possible switching states are finite. The 2L-3P VSI has 6 active and 2 zero space (or voltage) vectors. The six active vectors produce a nonzero output voltage and the two zero vectors produce zero output voltage (Table IV).

<table>
<thead>
<tr>
<th>Table IV: VSI Space Vectors</th>
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<tr>
<td>S1</td>
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<td>0</td>
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Space vector of the three phase quantities are represented as vectors in a two-dimensional aβ plane. For example, VAB shown in Fig. 5 represents S1S2S3=111 (Figs. 2 & 3) or vector V6 (S4S5S6=001) in Table III. VAB, VBC, and VCA are three 120° phase shifted line voltage vectors, represented by VAB=0, VBC=VDC, and VCA=-VDC for this case (Fig. 5) with an effective voltage/space) vector V6 equal to 1/3VDC - j\sqrt{3}/3VDC. In general,

\[
\vec{x} = x_a + jx_b
\]

(7)

Proceeding on similar lines the six non-zero voltage vectors (V1 - V6) can assume the positions shown in Fig. 6, forming a regular hexagon. There are six active vectors, with the area between any two adjacent vectors defined as a sector. The remaining two zero space vectors produce no output voltage, and therefore they remain at origin in aβ plane defining no sector. Space vector V is known as the reference space vector.

Note that all the 8 space vectors are stationary. The reference space vector, V (equivalent of Va and Vb at angle α), however rotates in space at angular velocity of ω (or 2πf). At any time, V is approximated by two active space vectors and a zero space vector. V rotates one revolution for one complete cycle of VAB and its length corresponds to the magnitude of VAB.

![Fig. 6 SVPWM space vectors](Image)

To generate any magnitude of VAB at the specific angle α, V0 and V3 followed by V and α are determined using (5), (8) and (9). Then the turn on durations T1, T2, and T0 are estimated with appropriate switching sequence and switching time for switches (S1-S3) using (10) to (12).

\[
\vec{v} = \sqrt{v_a^2 + v_b^2}
\]

(8)

\[
\alpha = \tan^{-1} \frac{v_b}{v_a} = \omega t = 2\pi ft
\]

(9)

\[
T_1 = \frac{\sqrt{3}v_a}{v_{dc}} \sin \frac{\pi}{3} \cos a - \cos \frac{\pi}{3} \sin a
\]

(10)

\[
T_2 = \frac{\sqrt{3}v_a}{v_{dc}} \left( -\cos \alpha \sin \frac{(n-1)\pi}{3} + \sin \alpha \cos \frac{(n-1)\pi}{3} \right)
\]

(11)

\[
T_0 = T - T_1 - T_2
\]

(12)

where T = 1/f, n=1-6 (sectors), and 0° ≤ α ≤ 60° [4].

There are a few options for null vector: V0 only, V7 only, or a combination of both [5]. The type of null vector determines the SVM technique. Based on the type of null vector different SVPWM techniques such as right aligned, symmetric, alternating zero vector and highest current not switched sequences [5] are obtained. All the techniques have almost identical DC source utilization; however, they differ in terms of switching loss and THD. Usually the optimal choice is 7-segment switching sequence (Fig. 7) which ensures minimum switching per sampling period and low harmonics.
IV. MPC

Model predictive control strategies like SVPWM take advantage of the fact that only a finite number of possible switching states are associated with VSI. These states are discrete and the model of the system can be used in association with a discrete-time model of the load to predict the behavior of the VSI system. A selection criterion, the objective function, is defined for selection of the optimal future variables corresponding to the optimal future switching state that minimizes the objective function. The objective of the MPC scheme is to predict switching state and thus currents that track the reference currents with minimal error. For each sampling period predicted, the output currents are measured and compared with the reference currents to minimize error. Usually the sampling period is chosen as a period in which the reference current does not change significantly; therefore, the reference current can be considered a constant for that period.

Equation (16) is used to predict the load current for each switching possibility. The objective function is evaluated for each of the eight possible voltage vectors generated by the VSI in order to calculate the future optimal value of the load current. The optimal value of the objective function is applied during the next sampling period. Fig. 8 shows the VSI MPC control system.

Fig. 9 outlines step by step implementation process of MPC for predictive control of VSI. First all the variables are initialized and values assigned (Data). This is followed by a process dynamic model of VSI based on the values for voltage vectors \( V_0 \) to \( V_7 \) (Table IV). Then a set of control actions or manipulated variables \( U_{(k)} \) based on operational principles (process experience) is developed corresponding to all possible output states (using switching states, \( S_6 S_4 S_2 \)) in Table IV. Current states of the system are measured (\( i_o(k) \)) including switching states of \( v(k) \) and future outputs \( Y_{(k+1)} \) (future currents \( i_{o(k+1)} \)) for \( k=1 \) to \( N \) are predicted using (16).

V. SYSTEMS PERFORMANCE ASSESSMENT

VSI output is ideally sinusoidal and independent of load parameters; however, the output of the practical inverter is non-sinusoidal and contains harmonics. The performance of VSI is therefore evaluated in terms of total harmonic distortion (THD), Distortion Factor (DF), and lower harmonic distortion (LOH) [3]. In addition, power loss, control complexity, implementation ease, and cost are also considered when evaluating VSI and the associated control plan.

The ratio of the root mean square (rms) and sum of total harmonic components of output voltage plus rms of the fundamental component, \( V_{0f} \), is called THD. It is the measure of closeness in shape between a waveform and its \( V_{0f} \). The LOH is the harmonic component with a magnitude greater than or equal to 3% of \( V_{0f} \).
To compare SVPWM and MPC control for 2L-3P VSI, Advanced Energy AE 100TX, a commercially available VSI from [6] with rated kVA = 100kW, rated \( V_{LL} = 600V \), rated \( I_o = 96A \), and \( f=60 \text{ Hz} \) is selected.

For \( m_a=1.0 \), DC link voltage is:

\[
V_{DC} = \sqrt{2} \frac{V_{LL}}{m_a} \approx 850V
\]  

Using per unit system, the base quantities are:

\[
V_B = \frac{V_{LL}}{\sqrt{3}} = 347V
\]  

\[
I_B = \frac{I_o}{\sqrt{3}} = 96A
\]  

\[
Z_B = \frac{V_B}{I_B} = 3.62 \Omega
\]  

\[
\omega_B = 2\pi f = 376.8 \text{ rad/s}
\]  

\[
L_B = \frac{I_B}{\omega_B} = 9.6 \text{ mH}
\]  

For 1 pu load impedance, \( L_L \) is 0.31 pu and \( R_L \) equals to 0.95 pu, given by (25) and (26):

\[
L_L = 0.31L_B = 3 mH
\]  

\[
R_L = 0.31R_B = 3.44 \Omega
\]  

Finally sampling time, \( T_s \), is set to 5\( \mu \text{s} \) for simulation. Fig. 10 shows the system modelled and simulated for both SVPWM and MPC.
Fig. 13 Fundamental and THD of $V_a$ (SVPWM-NF)

Fig. 14 shows the output of VSI with MPC control and without LC filter (NF). Note that $I_a$ is almost a pure sine wave, even without an LC filter.

Fig. 14 $I_a$, $I_a$-rms, $V_{ab}$, and $V_a$ (MPC-NF)

Figs. 15 and 16 show $I_a$ and $V_a$ fc and THD analysis. Both figures indicate great improvement in THD which is down from 10.98% to 0.25% and 51.68% to 34.31% for $I_a$ and $V_a$ respectively.

Fig. 15 Fundamental and THD of $I_a$ (MPC-NF)

Figs. 16 Fundamental and THD of $V_a$ (MPC-NF)

Figs. 17 and 18 show VSI output with SVPWM and MPC controls with LC filter (YF). The inductor (L) and capacitor (C) used are 4 mH and 370 µF respectively. Closer observation of Fig. 17 reveals that as expected $I_a$-rms≈96 A, $V_{ab}$-rms≈600 V and $V_a$-rms≈347 V; however, there is slight harmonics mix. Fig. 18 shows the same results for $I_a$-rms, $V_{ab}$-rms and $V_a$-rms with less harmonics.

Fig. 17 $I_a$, $I_a$-rms, $V_{ab}$, and $V_a$ (SVPWM-YF)

Fig. 18 $I_a$, $I_a$-rms, $V_{ab}$, and $V_a$ (MPC-YF)

Table V lists fc magnitude and THD for the waveforms in Figs. 17 and 18.
VII. CONCLUSION

This paper compared SVPWM with MPC for 2L-3P VSI. MPC was found superior in terms of THD and was easier to implement.

REFERENCES


<table>
<thead>
<tr>
<th>TABLE V</th>
<th>Va AND Ia FUNDAMENTAL AND HARMONICS</th>
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<tbody>
<tr>
<td></td>
<td>Control</td>
</tr>
<tr>
<td>SVPWM</td>
<td>135.2</td>
</tr>
<tr>
<td>MPC</td>
<td>134.6</td>
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