Li₄SiO₄ Prepared by Sol-gel Method as Potential Host for LISICON Structured Solid Electrolytes

Syed Bahari Ramadzan Syed Adnan, Nor Sabirin Mohamed and Norwati K.A

Abstract—In this study, Li₄SiO₄ powder was successfully synthesized via sol gel method followed by drying at 150°C. Lithium oxide, Li2O and silicon oxide, SiO2 were used as the starting materials with citric acid as the chelating agent. The obtained powder was then sintered at various temperatures. Crystallographic phase analysis, morphology and ionic conductivity were investigated systematically employing X-ray diffraction, Fourier Transform Infrared, Scanning Electron Microscopy and AC impedance spectroscopy. XRD result showed the formation of pure monoclinic Li_4SiO_4 crystal structure with lattice parameters a = 5.140 Å, b =6.094 Å, c = 5.293 Å, $\beta = 90^{\circ}$ in the sample sintered at 750°C. This observation was confirmed by FTIR analysis. The bulk conductivity of this sample at room temperature was 3.35 \times 10 6 S cm 1 and the highest bulk conductivity of 1.16×10^{-4} S cm⁻¹ was obtained at 100°C. The results indicated that, the Li₄SiO₄ compound has potential to be used as host for LISICON structured solid electrolyte for low temperature application.

Keywords— Conductivity, LISICON, Li₄SiO₄, Solid electrolyte, Structure.

I. INTRODUCTION

CRYSTALLINE as well as glassy Li₄SiO₄-based compounds have been investigated with regard to their use as solid electrolytes in secondary lithium batteries. The structure of Li₄SiO₄ has moderately good Li⁺ conductivity and is a versatile host structure for doping to form LISICON (Lithium Super Ionic) structure. Both Li⁺ interstitials and Li⁺ vacancies can be created resulting in high conductivities [1]. For example, when Si⁴⁺ ions are substituted by Al³⁺ ions, the Al³⁺ ions occupy Si⁴⁺ sites with Li⁺ ion entering interstitial sites, (Si⁴⁺ = Al³⁺ + Li⁺) to give a compound with a general formula, Li_{4+x}Al_xSi_{1-x}O₄ and when Si⁴⁺ ions are substituted by V⁵⁺ ions, Li⁺ vacancies are created, (Si⁴⁺ + Li⁺ = V⁵⁺) to give a compound with a general formula, Li_{4-x}V_xSi_{1-x}O₄[2].

LISICON structured solid electrolytes based on Li_4SiO_4 have been reported to show high conductivity at high temperatures (200°C – 500°C) [3]. These compounds are commonly synthesized by conventional solid state reaction method. This method are lead to many problems such as its use of high firing temperature, contamination with impurities, volatilization, lack of control of the microstructure and composition and suffer to obtaining good ceramics free of grain boundary resistances.

Syed Bahari Ramadzan Bin Syed Adnan is with University Of Malaya.Malaysia. e-mail:syed_bahari@yahoo.com

In order to overcome this problem, the sol gel method was used to prepare ion conductor materials. The sol gel technique has the advantages of lowering the synthesis temperature and can improving grain boundary conductivity and is a high purity process which leads to good homogeneity [4].

Recently Wu et al. reported the synthesis of Li_4SiO_4 employing sol gel technique [5-6]. In the former paper, the conductivity behavior of the compound was reported for the temperature range from 250 to 400°C. In the present study, Li_4SiO_4 compound was synthesized using a similar method. However, its conductivity behavior was studied at low temperatures ranging from 25 to 100°C in order to study the potential of this compound to be used as a host in LISICON structured solid electrolytes for low temperature electrochemical devices.

II. EXPERIMENTAL PROCEDURE

A. Synthesis of Li₄SiO₄

Li₄SiO₄ powder was prepared by sol gel method. Li₂O and SiO₂ were used as the starting materials and citric acid (C₆H₈O₇) was used as the chelating agent. The molar ratio of Li₂O: SiO₂ was fixed at 4:1. Li₂O was dissolved in distilled water before mixing with SiO₂ and citric acid. Citric acid was used to adjust the pH value of the solution to alkali (pH = 8.5). After that, the mixture was stirred under continuous reflux process for 1 hour until a colloidal solution was obtained. The colloidal solution was vaporized at 80°C and a gel was formed ultimately. The gel was dried at 150°C for 24 hours to remove H₂O particle, resistance organic group and also to avoid ceramic cracks. The flowchart of the synthesis procedure is shown in Figure 1. The obtained powder was palletized and the pallets formed were later sintered at temperatures from 600 – 750°C for four hours.

B. Characterization Techniques.

Crystallographic phases present in the prepared samples were identified by XRD using Bruker AXS, with CuK α radiation. To confirm the formation of the phases, FTIR was done on the samples employing Perkin Elmer FTIR Spectrum RX1 Spectrometer. The FTIR spectra were recorded at a resolution of 1 cm⁻¹. The morphology of the samples was observed by the Energy Dispersive Xray (EDX) technique. The AC impedance measurements were carried out using impedance analyzer, SOLATRON 1260 with platinum as the blocking electrode in the temperature range from 25 to 300°C over a frequency range from 0.1 to 10⁶ Hz.



Fig. 1 Flow chart of sol gel procedure for the preparation of Li_4SiO_4 powder.

III. RESULT AND DISCUSSION

A. X-ray Diffraction.

Figure 2 shows XRD spectra of Li₄SiO₄ sintered at different temperatures for four hours. As can be seen in the figure, the samples sintered at 600-700°C exhibit sharp diffraction peaks attributed to Li₄SiO₄ and Li₂CO₃ indicated the presence of both Li₄SiO₄ and Li₂CO₃ in the samples. The XRD spectrum of the sample sintered at 750°C shows peaks attributed only to Li₄SiO₄ demonstrating that pure Li₄SiO₄ has been obtained. The crystal structure of Li₄SiO₄ is monoclinic with space group $P2_{1/m}$ and lattice parameters are a = 5.140 Å, b = 6.094 Å, c = 5.293 Å, $\beta = 90^{\circ}$ which are close to those of West et al.[7].

B. Fourier Transform Infrared

Figure 3 depicts the FTIR spectra of the Li_4SiO_4 sample, sintered at 700°C and 750°C. The sample sintered at 700°C show strong bands in the regions of 726-1071 cm⁻¹ and 1366-1588 cm⁻¹. This may be attributed to stretching and bending vibrational modes of Si-O (800 cm⁻¹) in SiO₄ tetrahedral [8-9], stretching and bending vibrational modes of C=O (1550-1650 cm⁻¹) in CO₂ of Li₂CO₃ [10].

The Li₄SiO₄ sample sintered at 750°C does not show the bands between 1366 and 1588 cm⁻¹ which is due to the disappearance of C=O from Li₂CO₃. This observation is consistent with the result of XRD study and hence, confirms the formation of pure Li₄SiO₄ phase in the sample sintered at 750°C.



Fig. 2 XRD pattern of samples sintered at different temperatures.



Fig. 3 FTIR spectra for Li_4SiO_4 sample sintered at (a) 700°C and (b) 750°C

C. SEM and EDX analysis

The SEM micrograph of the sample sintered at 750°C is shown in Figure 4 (a) and its EDX analysis is presented in Figure 4 (b). The atomic ratio of Si:O is determined to be 1:4. Since Li^+ ion is not detectable by EDX, the concept of charge neutrality is employed [11]. Using this concept it is found that the stoichiometry of EDX analysis is in good agreement with the designated stoichiometry. This observation is consistent with the result of XRD analysis. Carbon detected in this analysis may come from citric acid.



Fig. 4 a) SEM micrograph and (b) EDX analysis of Li₄SiO₄ sample sintered at 750°C

D. Ionic Conductivity

Figures 5(a) and 5(b) display impedance spectra of the sample sintered at 750°C recorded at 27°C and 100°C. The conductivity value of the bulk conductivity was calculated using (1):

$$\sigma_{\rm b} = \frac{d}{AR_{\rm b}} \tag{1}$$

where *d* is the sample thickness, *A* is the area of the electrode and R_b is the bulk resistance. The bulk conductivity of this sample at 27°C is found to be 3.35×10^{-6} S cm⁻¹. The conductivity is observed to increase with increase in temperature. The increase in conductivity could be attributed to the greater movement of ionic point defect and the creation of greater ions movement due to an increase in thermal of energy [12]. The conductivity of the sample is determined to be 1.16×10^{-4} S cm⁻¹ at 100° C. The value is an order of magnitude higher compared to the Li_{4-3x}Ga_xSiO₄ (a modified Li₄SiO₄) system reported by Smith et al. measured at the same temperature [13].

The temperature dependence of the bulk conductivity is illustrated in Fig. 6 is found to be linear and well fits the Arrhenius equation (2),

$$\sigma_b T = A \exp\left(\frac{-E_{\alpha}}{kT}\right) \tag{2}$$

where A is the pre-exponential factor, E_{α} the activation energy for conduction and k the gas constant. This indicates that there are no structure and phase changes in the sample for the studied temperature range



Fig. 5(a): Impedance plot of Li_4SiO_4 at room temperature (R_b = bulk resistance). The figure in insert focuses on low impedance region.



Fig. 5(b): The conductivity of Li_4SiO_4 at 100°C (R_b = bulk resistance). The figure in insert focuses on the low impedance region.

Activation energy normally includes energy for formation and migration of ions. The Li₄SiO₄ structure is in the extrinsic regime below 200°C [14]. In this extrinsic regime, the activation energy is dominated by the migration energy. In this case, the activation energy can be represented by the migration energy for the doped oxide ionic conductors [15]. The activation energy; E_a determined from Figure 6 is 0.19 eV. The low value of activation energy is evidence for high mobility of ions in the sample.



Fig. 6 Arrhenius plot of the bulk conductivity for Li₄SiO₄ sample sintered at 750°C

IV. CONCLUSIONS

A pure Li_4SiO_4 sample was successfully prepared by sol gel method followed by sintering at 750° C. The conductivity of the Li_4SiO_4 compound obtained in this study showed conductivity in the order of 10^{-4} S cm⁻¹ at 100° C and is higher compared to the modified Li_4SiO_4 compound reported by other researchers. This gives an indication of its potential to be used as a host for low temperature LISICON structured solid electrolytes.

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Syed Bahari Ramadzan Bin Syed Adnan received the B.Sc. degree from University of Technology Malaysia, Skudai, Malaysia in 2006 and M.Sc. degree from University of Malaya, Kuala Lumpur, Malaysia in 2010, both in field of Science (Physics). He currently has joined the Department of Physics at Centre for Foundation Studies in Science, University of Malaya as a Ph.D. student. His research interests are Inorganic crystalline material especially in LISICON type structure as solid electrolytes.

Nor Sabirin Mohamed received the B.Sc. degree from State University of New Tork, Albany, USA in 1985, the M.Sc. degree from the same University in 1987 (both in field of Physics) and the Ph.D. degree in material field from University of Malaya, Kuala Lumpur, Malaysia, in 2004. She is currently with the Department of Physics at Centre for Foundation Studies in Science, University of Malaya. Her research interests are solid State Ionic including solid electrolyte, glass ceramic, polymer electrolyte and electrode materials.