

Electrodermal Activity Measurement Using Constant Current AC Source

Cristian Chacha, David Asiain, Jesús Ponce de León, José Ramón Beltrán

Abstract—This work explores and characterizes the behavior of the AFE AD5941 in impedance measurement using an embedded algorithm that allows using a constant current AC source. The main aim of this research is to improve the exact measurement of impedance values for their application in EDA-focused wearable devices. Through comprehensive study and characterization, it has been observed that employing a measurement sequence with a constant current source produces results with increased dispersion but higher accuracy and a more linear behavior with respect to error. As a result, this approach leads to a more accurate system for impedance measurement.

Keywords—Electrodermal Activity, constant current AC source, wearable, precision, accuracy, impedance.

I. INTRODUCTION

THE study of emotional intelligence has been present during the last decades, showing itself as the key point in improving the quality of life. Numerous studies have shown that the state of mind and the management of emotions are directly related to the physical conditions a person may present and the effectiveness while performing activities [1], [2], as well as improvements in various areas of daily life. According to [3], the first step to acquiring greater awareness about emotional intelligence and the management of our emotions is perception.

Several investigations on electrodermal activity (EDA) [4]-[7] show a high correlation between skin impedance values and stress levels, as well as other emotions such as fear, anxiety and various cognitive states. This correlation persists even when we are asleep. Therefore, the measurement of EDA has many advantages in recording, analyzing, and recognizing emotional states.

Skin impedance is related to the behavior of the parasympathetic system [8], [9] demonstrating correlation between skin impedance and the behavior of the parasympathetic system. These resources emphasize that this is a system over which we have no conscious control and is in turn related to various organs of the body, including the skin and specifically the sweat glands.

EDA opens up the possibility of measuring changes in skin conductivity or impedance without the use of intrusive methods, which has led to the development of wearable devices [10]-[14], although currently many of these devices only make use of DC voltage methods.

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II. ELECTRODERMAL ACTIVITY

EDA is the study of the characteristics present in the electrical signals produced by manifestations of the parasympathetic system, which is innervated by sweat glands [15]. This way, changes in the parasympathetic system create greater activity in the sweat glands present in the skin, which in turn generate sweat. The composition of sweat is mainly NaCl [9], so the dermis and epidermis exhibit a varying level of resistance to current flow. This, in turn, indicates a change in skin impedance.

Different layers of the skin can be modeled using passive electrical components such as resistors and capacitors. However, this model may vary depending on the measurement methods and the interface used between the skin and the electronic device used for measurement [16]. Fig. 1 presents a comprehensive model of the skin and the dry/wet electrode interface.

Fig. 1 represents the electrical model of the skin, it considers the epidermis and dermis layer as most important part; we note that the subscript ep refers to the epidermis, while the subscript sw is related to the activity of sweat glands pertaining to sweat related to the activity of sweat glands [16]. The electrical model of the epidermis and sweat parts consists of the parallel of a resistance and a capacitance, in series with a voltage source. The passive components can be considered as an equivalent impedance. The variation of resistance (R_{SW}) is related to sweat gland activity. The variation of C_{SW} is also induced by sweat, since sweat, in the sweat gland ducts, acts as the dielectric of a capacitor. The values of C_{EP} , R_{EP} , and R_d (dermis resistance) are different for each user but show a low fluctuation [8].

Exosomatic EDA measurements can be carried out using DC and AC excitation voltages. The presence of the reactance component is achieved only when using an AC measurement technique. This is because the DC current does not produce any further disturbances once the capacitors are charged. In addition, the AC current source technique avoids electrode polarization, since the current flow is altered at each oscillation cycle. Therefore, the use of an AC source has at least two obvious advantages: it allows the use of dry electrodes and provides more information about changes in skin impedance.

III. EDA DEVICE

To measure skin impedance, Analog Devices AD-5941 are used. This is a smart Analog-Front-End (AFE) device, whose

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accuracy and precision will be characterized. The AD-5941 is a complete data acquisition system packaged in a 7 mm × 7 mm, 48-lead LFCSP. This device is responsible for generating an AC signal between two electrodes, measuring the voltage difference (V_{exc}) and the current value (I) across these two

points. With these two measures, the impedance can be calculated using (1) is used. In this equation, all involved values can have real and imaginary part.

$$Z_{UNKNOWN} = \frac{V_{exc}}{I} - R_{LIMIT} - \frac{1}{2 \cdot 100 \cdot \pi} \left(\frac{1}{C_{ISO1}} + \frac{1}{C_{ISO2}} \right) \quad (1)$$

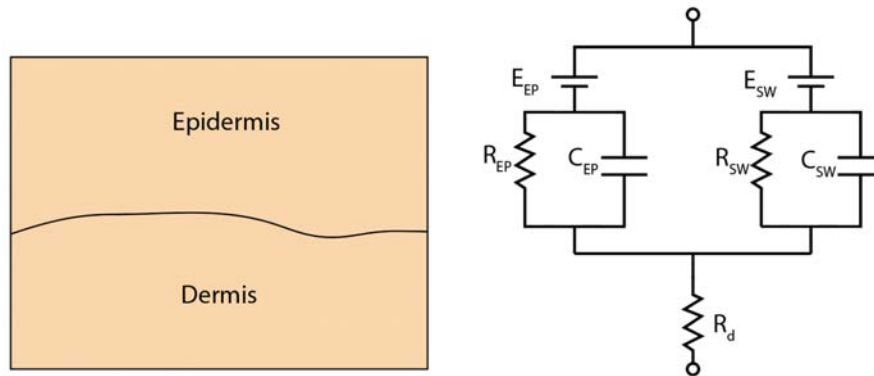


Fig. 1 Electrical model of the skin impedance

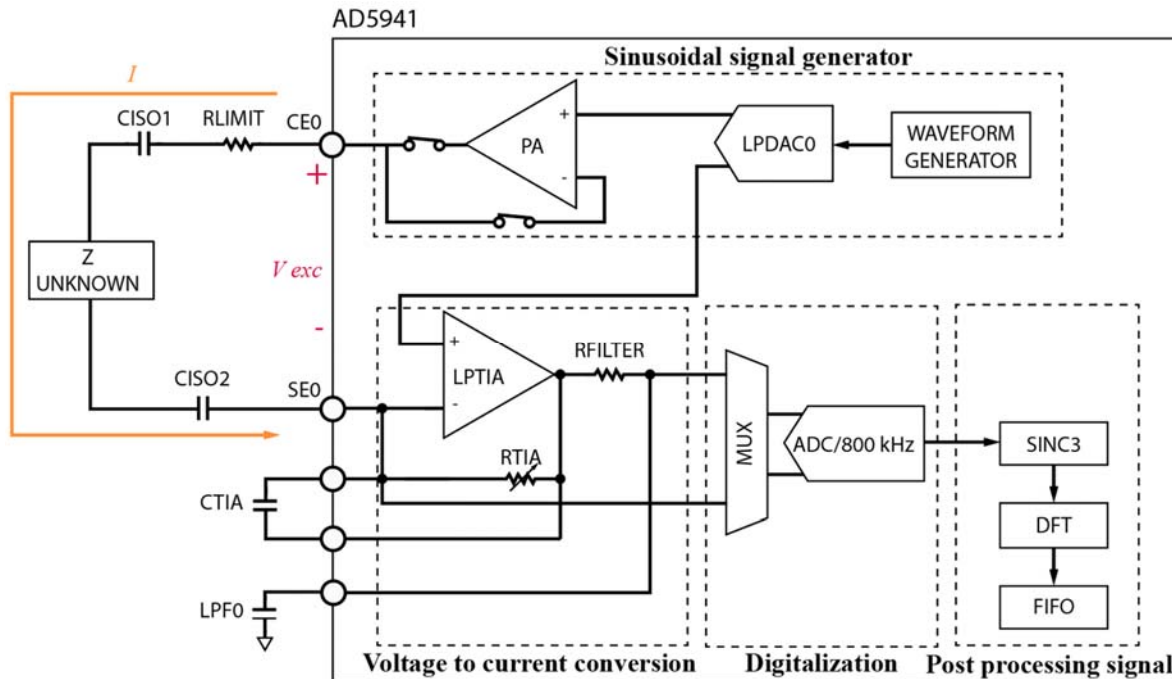


Fig. 2 Measurement of impedance using the AD-5941 IC

Fig. 2 shows a high-level vision of modules inside of the AD-5941. Four main parts can be distinguished: the sinusoidal signal generator, a current-to-voltage conversion, an ADC, and the post-processing of the signal.

The first stage incorporates a digital waveform generator which uses a low-power digital-to-analog converter (LPDAC). The LPDAC has two outputs: one with a 12-bit resolution, which is used to generate a sinusoidal waveform with an offset voltage, and the second output with a 6-bit resolution, which generates the offset voltage for the current-to-voltage converter stage.

The waveform generated by the LPDAC has a maximum

range value of 0.2-2.4V. To maintain the voltage value, a potentiostat (PA) is employed. The sinusoidal waveform V_{exc} , generated between the CE0 and SE0 pins, has a frequency of 100 Hz and an amplitude of 0.9 V without offset. Additionally, the system incorporates two capacitors to prevent direct current from flowing through the skin, in compliance with the IEC 60601 standard [17].

A capital part of this study focuses on the current to voltage converter stage. This conversion is done through a low power transimpedance amplifier (LPTIA). To perform this conversion, the LPTIA requires a feedback resistor (RTIA), which value is selected by the Micro Controller Unit (MCU)

based on input current. Since this current can vary from 50 pA to 3 mA [18], the AD-5941 features a RTIA range from 1 kΩ to 512 kΩ.

The selected value of RTIA is in parallel with a capacitor to reduce oscillation in the current-to-voltage conversion. Therefore, the feedback impedance and the entire set of resistors must be calibrated at the beginning of the measurement.

The digitalization stage uses an ADC capable of 800 kilo-samples per second. The use of a high sampling frequency is beneficial for the post-processing stage of the signal. The matrix configuration of the device allows using different inputs for the ADC, such as PA or LPTIA allowing for the measurement of voltage generated and current flowing through $Z_{UNKNOWN}$.

The use of such a high sampling frequency has advantages for the post-processing stage of the signal. In this last stage, a digital filter (SINC3) and a discrete Fourier transform (DFT) are used. This enables a frequency sweeping and the selection of magnitude and phase values for specific frequencies from the waveform generator in the first stage. The final step involves saving the information in a FIFO memory and transmitting voltage and current values, both in magnitude and phase, to the microcontroller using SPI communication.

It is worth mentioning that the AD-5941 features a programmable sequencer that allows for the execution of all measurement steps independently. This sequence needs to be programmed initially and it can also be modified in real-time by the microcontroller.

IV. MEASUREMENT OF IMPEDANCE

This section describes the implementation of the sequence used to perform an impedance measurement using a constant current source without the need to add other external hardware components to achieve it.

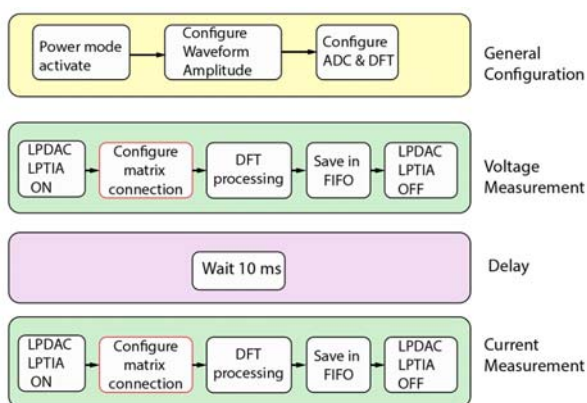


Fig. 3 Sequence load in AFE AD-5941

The main idea of this measurement method is to use the waveform generator as a generator of sinusoidal signals, with a variable amplitude but maintaining a frequency of 100 Hz. This generator operates within a limited range of voltage values. Fig. 3 shows the complete sequence that the MCU loads into the

AD-5941. This sequence is executed automatically every 250 ms, without MCU intervention, as will be detailed later.

As can be seen in Fig. 3, the first stage in the sequence is to configure all modules of the AD-5941 and switch to active power mode. In this step, the waveform is configured with a specific amplitude value for the sinusoidal signal. This first stage only involves configuration and does not power on any module.

The next stage involves the voltage measurement. The LPDAC and LPTIA modules are switched on and the matrix connection is configured to measure the voltage value of the PA (connected to the LPDAC). Additionally, the DFT processing signal is performed and saved in the FIFO. After this voltage measurement, the device switches off the modules and does not generate any voltage signal. In this stage, output voltage is not present in CE0 pin (blue path in Fig. 4), therefore there is not current flow in the $Z_{UNKNOWN}$ impedance.

There is a stage delay of 10 ms to achieve signal stabilization and discharge the passive components, thus avoiding erroneous measurements.

The current measurement stage is very similar to the voltage stage. The steps are the same, except for the configuration of the matrix connection, which allows us to connect the output of the PA to the CE0 pin and inject current through $Z_{UNKNOWN}$.

LPTIA is configured to has current input from pin SE0. Fig. 4 illustrates difference between connection to voltage measurement (blue path) and current measurement (orange path).

Taking into account that the value of the $Z_{UNKNOWN}$ impedance (and therefore the current flowing through it) can vary, a feedback current value is calculated, and a voltage amplitude adjustment is made. This adjustment is intended to ensure that the current flowing through $Z_{UNKNOWN}$ remains constant and is within a value close to but not exceeding 10 uA, in compliance with IEC 60601. Since the current range remains constant, the need to change or calibrate other feedback impedance values in the LPTIA is eliminated, so a single value of the RTIA feedback resistor is used.

A complete illustration of the sequence execution can be seen in Fig. 5. During the device initialization, the voltage signal has an amplitude of 1 mV. After four measurements and communication with the MCU, the embedded sequence is modified to generate a new amplitude in the sinusoidal signal.

As can be seen in Fig. 5, a voltage measurement is performed before each current measurement, unlike the algorithm proposed by Analog Devices, where a voltage measurement is only realized at the beginning of the sequence under the assumption that the value remains constant. This approach adds significant value to the measurement process as voltage and current samples are taken in each sample. Considering that the device may be subjected to environmental changes, a quasi-simultaneous measurement of voltage and current is achieved, resulting in a more accurate impedance measurement value.

During the development of the algorithm, it was observed that conducting multiple consecutive measurements of the applied voltage resulted in a high dispersion of these values, with a deviation of approximately 0.3%. To address this issue,

a window filter is applied in a post-processing stage within the MCU. This filter effectively reduces the voltage dispersion to a much lower value of 0.06%.

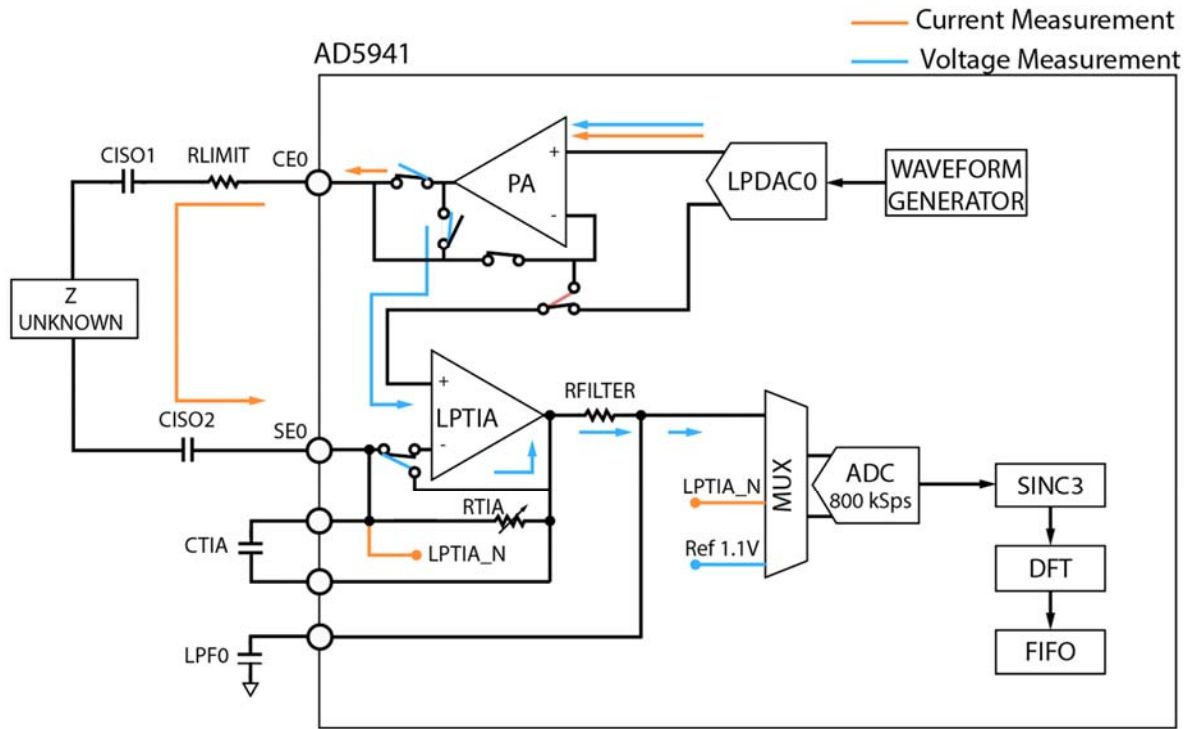
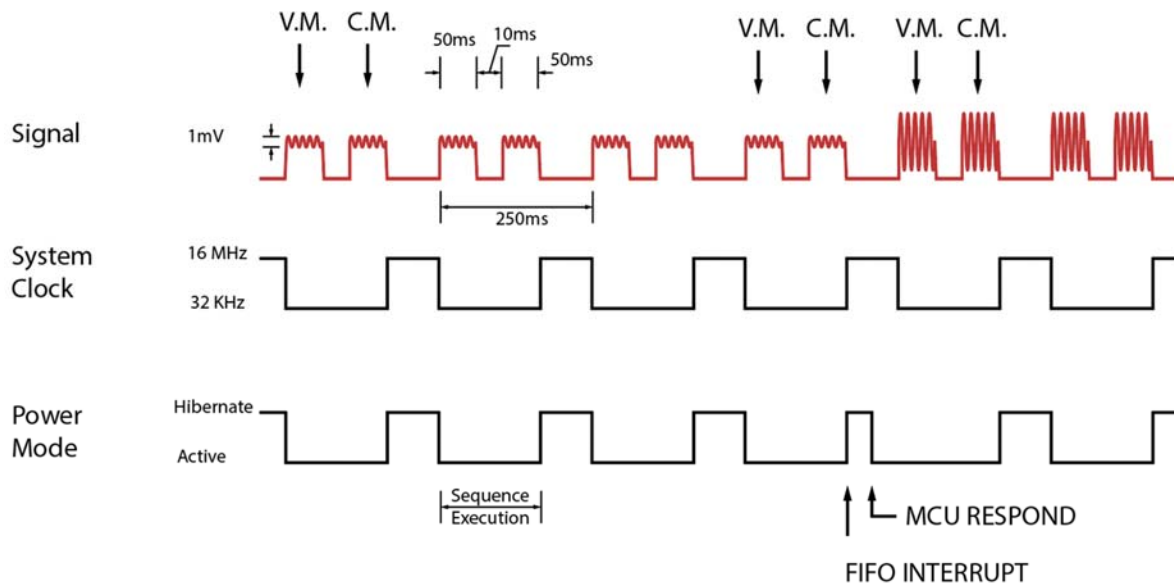


Fig. 4 Configuration of the connection matrix to measure voltage/current in AD-5941



V.M. = Voltage measurement
 C.M. = Current measurement

Fig. 5 Sequence of measurement using a constant current source

V. EXPERIMENT DESIGN

To check the precision and accuracy of the implemented algorithm, a test bench was prepared. This test bench consisted of a set of resistors with values between 50 kΩ and 1000 kΩ.

The objective of the test is to compare the performance of the algorithm proposed by Analog Devices with respect to the new proposed algorithm, using a constant current source.

To perform the comparison, a set of 200 samples was taken

for each resistance value. In (1), both algorithms were tested using a C_{ISO1} and C_{ISO2} value of 1 μF , and an R_{LIMIT} of 1 $k\Omega$ was used to enable measurement of low impedance values and demonstrate the feedback current mechanism to control the current under 10 μA . The objective of this test is to compare the changes in resistance and reactance as the value of the measured impedance increases.

VI. RESULTS

After analyzing the set of samples, we calculate the variation coefficient using standard deviation and mean value (2), a summary of the data dispersion values for each test can be observed in Table I.

$$CV = \frac{\sigma_x}{|\bar{x}|} = \frac{\sqrt{\frac{\sum(x_i - \bar{x})^2}{N \cdot |\bar{x}|}}}{|\bar{x}|} \quad (2)$$

A summary of the data dispersion values for each test can be observed in Table I. The first column shows all the resistor values used in the tests. In the second column, the dispersion value when using the constant voltage AC algorithm is shown (Analog Devices). Finally, the third column shows the dispersion value when using the algorithm proposed for a constant AC current source.

Resistance [$k\Omega$]	Dispersion coefficient [%]	
	Voltage AC constant source	Current AC constant source
50	0.015	0.0027
100	0.0041	0.0046
270	0.0023	0.0037
560	0.0036	0.0069
1000	0.0081	0.0133

We can see that dispersion values increase in the worst case in a factor of 1.6 when employing the constant current algorithm for impedance measurement. As can be seen from Table I, the dispersion coefficient is larger when measured at constant current than when measured at constant voltage. However, the error committed undergoes a smaller variation at constant current than at constant voltage. This trend is confirmed by comparing the relative error associated with the different algorithms in Fig 6. The tendency of the constant voltage algorithm is to make an increasing error as the measured resistance value becomes smaller. However, the measurement at constant current shows a flatter error over the entire measurement range, becoming reduced by a factor of 10 for the lower resistor values. This phenomenon can be attributed to the use of a single RTIA value and the amplitude modulation of the voltage signal to achieve a constant current flow.

Also, when analyzing the variations in resistance and reactance, (see Fig. 7) it is observed that the constant current method exhibits lesser changes in reactance. The obtained values approach zero, which was expected since the experiment solely involved varying the electrical resistance. In other words, it is observed that the constant voltage source method and changes in RTIA values for current-to-voltage conversion in

LPTIA can induce changes in reactance as the overall value of the LPTIA feedback impedance is changed.

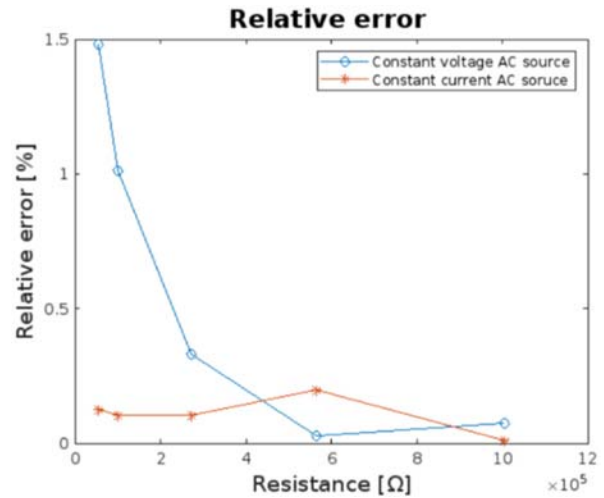


Fig. 6 Impedance measurement relative error

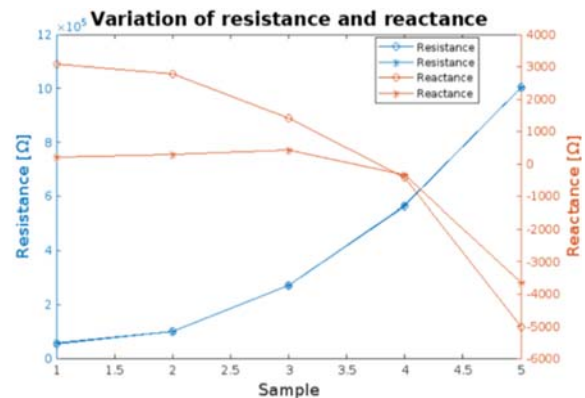


Fig. 7 Variation of resistance and reactance; in blue: constant voltage source, in orange: constant current source

VII. CONCLUSION

In conclusion, the proposed constant current algorithm shows a trade-off between data dispersion and accuracy. Although it generates a larger data dispersion, it also presents a lower relative error value, which ultimately improves the overall accuracy of the system. At the lower end of the measurement range, the algorithm achieves a remarkable 10-fold improvement in accuracy, although at a sacrifice of precision by a factor of 1.6. As a result, the system provides more accurate and reliable impedance values.

The use of an algorithm that allows the AD-5941 to be used as a constant current source is of great interest. This algorithm generates minimal alterations in the reactance values during impedance measurements, which can significantly improve the study of EDA. On the other hand, it ensures that changes in reactance are largely the result of individual variations and are not caused by the specific measurement system.

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