

# Solving Process Planning, Weighted Earliest Due Date Scheduling and Weighted Due Date Assignment Using Simulated Annealing and Evolutionary Strategies

Halil Ibrahim Demir, Abdullah Hulusi Kokcam, Fuat Simsir, Özer Uygun

**Abstract**—Traditionally, three important manufacturing functions which are process planning, scheduling and due-date assignment are performed sequentially and separately. Although there are numerous works on the integration of process planning and scheduling and plenty of works focusing on scheduling with due date assignment, there are only a few works on integrated process planning, scheduling and due-date assignment. Although due-dates are determined without taking into account of weights of the customers in the literature, here weighted due-date assignment is employed to get better performance. Jobs are scheduled according to weighted earliest due date dispatching rule and due dates are determined according to some popular due date assignment methods by taking into account of the weights of each job. Simulated Annealing, Evolutionary Strategies, Random Search, hybrid of Random Search and Simulated Annealing, and hybrid of Random Search and Evolutionary Strategies, are applied as solution techniques. Three important manufacturing functions are integrated step-by-step and higher integration levels are found better. Search meta-heuristics are found to be very useful while improving performance measure.

**Keywords**—Evolutionary strategies, hybrid searches, process planning, simulated annealing, weighted due-date assignment, weighted scheduling.

## I. INTRODUCTION

TRADITIONALLY three important manufacturing functions, process planning, scheduling and due-date assignment are processed separately. Although there are plenty of works on IPPS (Integrated Process Planning and Scheduling) and SWDDA (Scheduling with Due Date Assignment) problems, there are only a few works focusing on the IPPSDDA (Integrated Process Planning and Scheduling and Due Date Assignment) problem.

The job shop scheduling problem belongs to the NP-Hard problem class without any integration; integrated problems are even harder to solve. Thus, metaheuristics are commonly utilized in literature. In this research, OS (Ordinary Solution), RS (Random Search), SA (Simulated Annealing), ES

Evolutionary Strategies), RS/SA (Random Search and Simulated Annealing) Hybrid and RS/ES (Random Search and Evolutionary Strategies) Hybrid techniques are used.

According to the Society of Manufacturing Engineers, process planning is the systematic determination of the methods by which a product is to be manufactured economically and competitively. Scheduling allocates resources to tasks and considers timing information [1]. According to Gordon et al. [2], SWDDA problems have received considerable attention recently. According to classical inventory management strategy, only tardiness is an undesired outcome. On the other hand, according to JIT (Just-in-Time) both tardiness and earliness are undesired outcomes.

Since long due dates are unwanted it is reasonable to penalize due dates along with tardiness and earliness. In this study all of the weighted due-date, earliness and tardiness related costs are punished.

The weight of each customer is not considered when designating due dates in the literature. Contrary to this, important customers are given closer due dates and scheduled earlier and relatively less important customers are given long due dates and scheduled later in this study. In this way substantial improvement can be obtained for weighted penalty function. There can be substantial improvement in all of the weighted due-date, earliness and tardiness related costs if more important customers are given close due dates and scheduled earlier. These three important manufacturing functions are interrelated and outputs of upstream functions are inputs of downstream functions. For example, process plans become input to the scheduling function. Poorly prepared process plans become bad inputs to the downstream scheduling functions and may not be followed at the shop floor.

Independently performed functions try to reach local optimum and do not care about global optimum. For instance process planners may frequently select same desired machines but may not select some undesired machines at all. Therefore machine loading across production lane becomes unbalanced. But, if both the process planning and scheduling is taken into account simultaneously, process planners may be able to prepare more appropriate process plans and improve machine balancing at the shop floor. On the other hand, given due-dates may be unrealistic, as they might be given excessively far or insufficiently close, where they cannot be fulfilled.

Halil Ibrahim Demir is with the Sakarya University, Department of Industrial Engineering, Sakarya, Turkey (phone: +90 264 2955675; e-mail: hidemir@sakarya.edu.tr).

Abdullah Hulusi Kokcam and Özer Uygun are with the Sakarya University, Department of Industrial Engineering, Sakarya, Turkey (e-mail: akokcam@sakarya.edu.tr, ouygun@sakarya.edu.tr).

Fuat Simsir is with the Karabuk University, Department of Industrial Engineering, Karabuk, Turkey (e-mail: fuatsimsir@karabuk.edu.tr).

Unintegrated solution of the problem considerably degrades the performance function.

The problem is represented as a chromosome and every chromosome consists of  $(n+2)$  genes. The first two genes represent the due-date assignment and dispatching rules. The remaining genes represent the actively selected route of every job. Since the first two genes have a high impact on performance compared to the remaining genes, they have given higher probabilities to be selected for mutation, as the dominant gene approach is utilized in this research.

Problems are tested for each integration level. Initially, the unintegrated level is tested and later the integration degree is increased step-by-step by adding more functions to the problem. Higher integration levels give better results. All three functions are integrated at the highest integration level in which the best results are obtained. Full integration with directed search is found to be the best of all combinations, as expected.

## II. BACKGROUND AND LITERATURE SURVEY

In this study, three important manufacturing functions are integrated step-by-step. There are numerous works on IPPS and plenty of works on SWDDA, but only a few works focusing on the IPPSDDA problem.

Some important literature survey on IPPS can be found in [3], [4]. Although it is better to have alternative process plans, marginal benefit of alternative plans reduce sharply, and thus, the number of process plans should be determined wisely. Usher [4] studied the impacts of alternative process plans on manufacturing performance. Selecting a process plan among alternatives becomes more difficult as the number of process plans increases. Bhaskaran [5] studied process plan selection in his work.

Scheduling problem alone belongs to the NP-Hard class, and integrated problems are even harder to solve. Many of the researchers used some metaheuristics in their solution, as [6]-[14]. Some earlier works on IPPS can be found as [1], [15]-[22], [6]. Some recent works on IPPS can be found in [3], [4], [7], [9], [10], [13], [14], [23]-[31].

There are numerous works on SWDDA in the literature, which is another popular research problem. Again, it is better to look at literature surveys on this problem before mentioning other researches. A survey on scheduling with common due date assignment is prepared by Gordon et al. [2].

Sometimes common due dates are tried to be assigned for the parts to be assembled together or shipped to the customer at the same time. In this case, we should assign common due-date for the parts. The following works can be given as an example on scheduling with common due date assignments; [2], [32]-[40]. Some of the works are on scheduling with separate due date assignments. Following works can be given as an example to this problem; [41]-[47], [37].

There are several works on SMSWDDA (Single machine scheduling with due date assignment) such as [33], [44], [35], [2], [37], [38], [47], [48].

Reference [49] can be given as an example to two machine flow shop scheduling with due date determination. References

[43], [2], [50], [39] are examples to works on parallel machine scheduling with due date determination. References [51], [45], [52] are examples to job shop scheduling with due date determination. There are also works on MMSWDDA (Multi machine scheduling with due date assignment). References [41], [53], [54], and [36], can be given as an example to this problem.

## III. PROBLEM DEFINITION

In this research, the IPPSDDA problem is studied. Although plenty of works were conducted on the IPPS and SWDDA problems, research into the IPPSDDA problem is very new area and there are few studies available on this topic [55]-[58].

In order to solve the problem, simulated annealing and evolutionary strategies and some hybrid strategies are used. The problem is represented as a chromosome. Each chromosome is represented with  $(n+2)$  genes which represent due-date assignment rules, dispatching rules and actively selected routes of every job.

Integrated process planning, WEDD (Weighted Earliest Due Date Scheduling) scheduling, and WDUE (Weighted Due-Date Assignment) assignment problems are tested. Starting with the unintegrated problem where all functions are performed sequentially, every integration combination is tested and step-by-step functions are integrated with each other. Finally, all of the three functions are integrated and this level is found as the best integration level.

Four shop floors are tested in this research. The largest shop floor has 175 jobs, 35 machines and every job has 10 operations in every route. The first smaller two shop floors have five alternative routes and the other larger two shop floors have three alternative routes. The processing times of each operation of every job in every route changes according to normal distribution with a mean of 12 and a standard deviation of 6, according to the formula  $[(12+z*6)]$ . Processing times assume integer values, and so, they take values in between 1 and 30.

The characteristics of all shop floors are tabulated and presented in Table I, where SF is for Shop Floor, # of MC is for number of machines, # of J is for number of Jobs, # of R is for number of alternative routes, PT is for processing time and # of O is for number of operations.

TABLE I  
 CHARACTERISTICS OF SHOP FLOORS

SF	SF 1	SF 2	SF 3	SF 4
# of MC	5	15	25	35
# of J	25	75	125	175
# of R	5	5	3	3
PT	$[(12 + z * 6)]$	$[(12 + z * 6)]$	$[(12 + z * 6)]$	$[(12 + z * 6)]$
# of O	10	10	10	10

Initially, the unintegrated version is tested where all functions are performed separately. After that, WEDD dispatching is integrated with process plan selection but due dates are determined randomly. Later, WDUE assignment is

integrated with process plan selection, but this time, jobs are scheduled randomly. Finally, all functions are integrated and process plan selection, WEDD dispatching and WDUE assignments are performed concurrently.

In this study, one shift is assumed per day and it makes 480 minutes per day. As a penalty function, all of the weighted earliness, tardiness and due-date related costs are penalized according to the following formulas (1)-(5).

$$PD(j) = \text{weight}(j) * 8 * (\text{Due date}/480) \quad (1)$$

$$PE(j) = \text{weight}(j) * (5 + 4 * (E/480)) \quad (2)$$

$$PT(j) = \text{weight}(j) * (10 + 12 * (T/480)) \quad (3)$$

$$\text{Penalty}(j) = PD(j) + PE(j) + PT(j) \quad (4)$$

$$\text{Total Penalty} = \sum_j \text{Penalty}(j) \quad (5)$$

where PD is for penalty for due date, PE is for penalty for earliness, PT is penalty for tardiness, Penalty(j) is the total penalty occurred for job j and Total Penalty is the total penalty occurred for all jobs which is the ultimate desired value.

#### IV. METAHEURISTICS USED

In this study OS, RS, SA, RS/SA, ES, and RS/ES results are compared. OS represents initial random poor solutions, and RS is undirected search and scans solution space randomly. SA and ES are directed searches and gain benefit from earlier solutions and are better heuristics compared to RS. RS/SA and RS/ES are hybrid heuristics and are initially undirected, but later become directed searches. The reason for selecting hybrid searches is to benefit from both the power of an undirected search at the very beginning of the iterations, and later converting to the directed search and using the power of the directed search. The search techniques that have been utilized in this study are explained below.

**Ordinary Solution (OS):** Initially, totally a random chromosome is produced and this solution is assumed as ordinary solution. This is done to observe how ordinary solutions are poor and how directed and undirected search metaheuristics are powerful.

**Random Search (RS):** In this search, new chromosomes as many as in Evolutionary strategies are produced randomly. At every iteration, 13 new chromosomes are produced randomly. To be fair, in comparison with other search techniques, the same number of new chromosomes is produced for each search technique during the program execution.

**Simulated Annealing (SA):** SA was developed by Kirkpatrick et al. [59] in 1983 and used in many problems in numerous disciplines. Instead of working on population, this research focuses on a single chromosome in each iteration. For this reason, more iterations are made to be fair with other search techniques. The iteration parameters are tabulated and presented in Table II below.

**Random and Simulated Annealing (RS/SA):** In this

research, an initial 10% of iterations are random iterations and remaining 90% of iterations are SA iterations. Solution space is scanned with random search initially to be able to get a better improvement on the solution. After initial random iterations we continued with SA search. At Evolutionary strategies we work on population with size 10 and we produce new 13 chromosomes. Here at SA we work on single chromosome and produce one new chromosome. That is why to be fair in comparison, in this search iteration, size is 13 times as many as in the iteration size of Evolutionary strategies.

**Evolutionary Strategies (ES):** Evolutionary strategies were developed in the early 1960s. Two students from the Technical University of Berlin, Germany, introduced evolutionary strategies while solving optimization problems [60], [61]. Unlike genetic algorithms, ES uses only mutation operator. At every iteration, 13 new chromosomes produced; thus, fewer iterations are applied compared to the SA heuristics.

**Random and Evolutionary Strategies (RS/ES):** The initial 10% of iterations are random iterations and the remaining 90% of iterations are ES iterations; 13 new chromosomes are produced at every iteration. Thus, fewer iterations are applied compared to the SA and RS/SA search heuristics, but the same number of chromosomes is tested during the program execution.

RS, ES, and RS/ES hybrid searches are population based and 13 new chromosomes are produced at each iteration. For RS, ES, and RS/ES hybrid searches 200, 150, 100, and 50 iterations are applied, respectively. SA and RS/SA hybrid searches use only one chromosome at each iteration. Thus, to be fair with other methods, 2600, 1950, 1300 and 650 iterations are applied for four shop floors, respectively.

CPU times required for RS, SA, RS/SA, ES, and RS/ES search heuristics are summarized in Table V.

TABLE II  
 ITERATION NUMBERS FOR PURE AND HYBRID SEARCHES

SF	RS	SA	RS/SA Hybrid	ES	RS/ES Hybrid
	Random Iter.	SA Iter.	Random Iter.	SA Iter.	Random Iter.
1	200	2600	260	2340	20
2	150	1950	195	1755	15
3	100	1300	130	1170	10
4	50	650	65	585	5

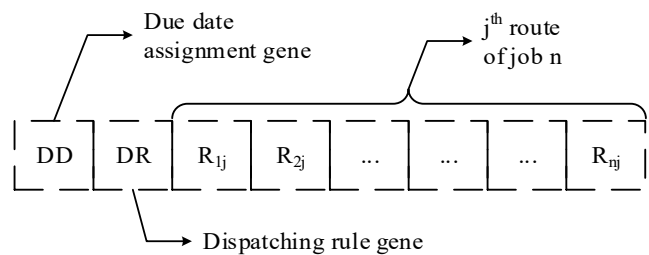


Fig. 1 Sample chromosome

Gene representation of the problem is illustrated in Fig. 1.

There are  $(n + 2)$  genes in a chromosome and the initial two genes represent the due-date assignment rules and dispatching rules, and remaining  $n$  genes represent the actively selected routes of every job.

The dominant gene approach is used in this study. Since initial two genes have higher impact on performance measure, higher probability for mutation is given. Remaining  $n$  genes have less effect on penalty function. Thus, lower probability is given to be selected for mutation.

Five types of rules are used while assigning due dates, which are the WTWK (Weighted Total Work), WSLK (Weighted Slack), WPPW (Weighted Process Plus Wait), WNOP (Weighted Number of Operations), and RDM (Random) rules. With the different multipliers and constants, 19 rules are used to assign due dates. These rules are tabulated in Table III and explained in Appendix A.

TABLE III  
DUE-DATE ASSIGNMENT RULES

Method	Multiplier k	Constant $q_x$	Rule no
WTWK	$k = 1, 2, 3$		0, 1, 2
WSLK		$q_x = q_1, q_2, q_3$	3, 4, 5
WPPW	$k = 1, 2, 3$	$q_x = q_1, q_2, q_3$	6, 7, 8, 9, 10, 11, 12, 13, 14
WNOP	$k = 1, 2, 3$		15, 16, 17
RDM			18

TABLE IV  
DISPATCHING RULES

Method	Rule No
WEDD	1
SIRO	2

The second gene is about scheduling rule and takes one of two values. Two rules are used, which are WEDD (Weighted Earliest Due Date) and SIRO (Service In Random Order) rules. These rules are summarized in Table IV and explained in Appendix B.

#### V. SOLUTIONS COMPARED

In this study, OS, RS, SA, RS/SA, ES, and RS/ES search solutions are compared with each other. At every integration level, six search heuristics are compared. There are four different integration levels and in total, 24 different combinations for each one of the four shop floors compared. The results are summarized in Table V. Next, each integration level is summarized.

**SIRO-RDM (OS, RS, SA, RS/SA, ES, RS/ES):** This is the lowest level of integration, where all functions are disintegrated. All search techniques which are OS, RS, SA, RS/SA, ES, and RS/ES heuristics are compared.

**WEDD-RDM (OS, RS, SA, RS/SA, ES, RS/ES):** Here, the WEDD dispatching rule is integrated with process plan selection, but the due dates are still determined randomly. Here substantial improvements are observed. All search techniques which are OS, RS, SA, RS/SA, ES, and RS/ES heuristics are compared, where OS solutions are very poor and searches are found useful. Directed searches and hybrid searches are found superior compared to the undirected search.

**SIRO-WDUE (OS, RS, SA, RS/SA, ES, RS/ES):** Later, WDUE assignment is integrated with process plan selection, but this time, jobs are dispatched in random order. Although integrating WDUE improves the global performance substantially, SIRO dispatching sharply deteriorates the global performance back.

**WEDD-WDUE (OS, RS, SA, RS/SA, ES, RS/ES):** Finally, three important manufacturing functions are integrated, and this is the highest integration level that gives the best global performance.

#### VI. EXPERIMENTATION

The integrated problem is coded using C++ programming language and run using a Borland C++ 5.02 compiler on a laptop with a 2 GHz processor, 8 GB Ram, and CPU times are recorded and summarized at Table V.

A chromosome is used to represent the integrated problem. Each chromosome consists of  $(n + 2)$  genes to represent due-date assignment rules, dispatching rules and selected routes of every job. Five different rules are used in due date assignment with various multipliers and constants which sums up to 19 different rules. WEDD and SIRO are used as dispatching rules.

Four shop floors in which the characteristics are given at Table I are tested. Initially, an unintegrated combination, which is SIRO-RDM, is tested. Later, the WEDD-RDM combination is tested, where WEDD scheduling and process plan selection are integrated. After that, the SIRO-WDUE combination is tested and WDUE due date assignment methods are integrated with process plan selection. Finally, the WEDD-WDUE combination is tested, where all three functions are integrated. Here, weighted due date assignment and weighted scheduling are applied and important customers are given closer due dates and scheduled earlier. This provides substantial improvement in performance function, which is weighted penalty on earliness, tardiness and due-date related costs.

Four shop floors are tested for 24 different solution combinations and the results summarized in Table V. At the first shop floor there are 25 jobs and 5 machines. Every job has 10 operations at each route. It took approximately 20 seconds CPU time to run the program. The results are summarized in Table V and Fig. 2. Full integration level was found to be the best integration combination. At the integration levels, SA heuristic gave the best result once, ES gave the best result once and RS/ES hybrid heuristic gave the best results three times. At the full integration level, the best results are shared between ES and RS/ES heuristics.

At the second shop floor, it took approximately about 200 seconds to complete the run of the program. The results are summarized in Table V and Fig. 3. According to the results, SA found twice the best solutions in integration levels and ES found twice the best solutions in the remaining two levels of integration. Again, full integration level is found as the best integration level.

At the third shop floor it took in between 200 and 300

seconds to complete the run of the program. The results are illustrated in Table V and Fig. 4. In two levels of integration, RS/SA gave best results, while the remaining two levels ES gave best results and ES and RS/ES shared best result in one level of integration. Again, at this shop floor the full integration level is found as the best combination as expected.

At the fourth shop floor, which is the largest shop floor with

175 jobs and 35 machines, it took about 300 seconds to complete the run of the program. The results are summarized in Table V and Fig. 5. According to the results, SA gave the best result in one level, ES gave the best results in two levels and RS/ES gave the best result in the remaining level. Full integration level was found the best integration level, as expected.

TABLE V  
 COMPARISON OF TWENTY FOUR SOLUTION COMBINATIONS FOR ALL OF THE SHOP FLOORS

Level of Integration (Combination)	Approaches	Shop Floor 1				Shop Floor 2				Shop Floor 3				Shop Floor 4			
		Best	Avg.	Worst	CPU	Best	Avg.	Worst	CPU	Best	Avg.	Worst	CPU	Best	Avg.	Worst	CPU
SIRO-RDM	OS	293	293	293	-	906	906	906	-	1413	1413	1413	-	2020	2020	2020	-
	RS	268	273	275	37	853	864	870	392	1355	1372	1378	286	1908	1925	1934	296
	SA	256	260	261	35	825	838	844	196	1358	1365	1369	240	1882	1896	1903	247
	RS/SA	258	263	266	34	846	857	864	346	1292	1318	1326	238	1876	1888	1899	246
	ES	256	260	263	37	826	838	844	405	1315	1323	1329	276	1860	1871	1879	286
WEDD-RDM	RS/ES	248	252	255	38	827	835	839	379	1322	1325	1327	276	1861	1875	1885	289
	OS	226	226	226	-	683	683	683	-	1122	1122	1122	-	1616	1616	1616	-
	RS	212	214	215	18	666	675	677	214	1060	1070	1081	286	1500	1513	1520	294
	SA	204	206	207	17	658	661	663	194	1041	1050	1052	245	1474	1489	1495	249
	RS/SA	207	208	209	17	663	668	670	195	1030	1040	1047	243	1484	1501	1508	255
SIRO-WDUE	ES	205	206	207	17	652	654	655	207	1040	1046	1048	274	1469	1480	1487	286
	RS/ES	208	208	208	18	653	657	659	209	1031	1041	1045	276	1467	1479	1485	287
	OS	337	337	337	-	1032	1032	1032	-	1570	1570	1570	-	2265	2265	2265	-
	RS	271	273	276	20	865	872	877	225	1328	1339	1349	299	1886	1907	1923	306
	SA	265	270	273	19	861	868	873	203	1312	1333	1342	254	1876	1900	1915	260
WEDD-WDUE	RS/SA	268	276	278	19	851	862	868	202	1319	1328	1336	255	1894	1907	1920	260
	ES	257	262	265	20	823	848	855	222	1295	1313	1324	294	1879	1896	1907	299
	RS/ES	255	258	260	20	842	852	858	222	1302	1320	1327	294	1880	1896	1909	302
	OS	271	271	271	-	865	865	865	-	1318	1318	1318	-	1894	1894	1894	-
	RS	192	197	199	20	610	622	627	225	948	957	963	303	1357	1376	1386	314
WEDD-WDUE	SA	188	190	192	18	599	610	615	206	943	953	958	265	1355	1373	1386	273
	RS/SA	189	191	193	18	601	609	613	206	947	955	959	268	1357	1378	1386	273
	ES	185	187	188	20	588	595	599	221	931	939	944	294	1345	1363	1375	309
	RS/ES	185	188	189	20	593	602	606	222	931	939	944	297	1349	1369	1380	310

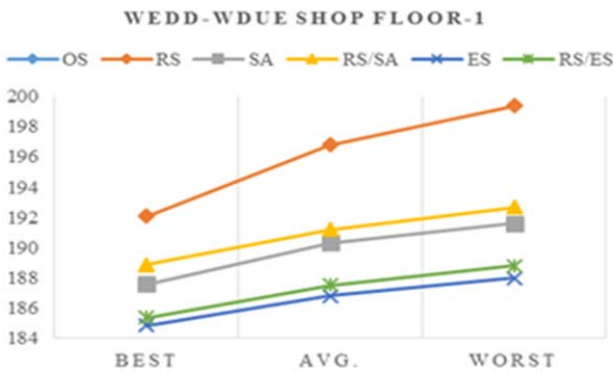


Fig. 2 Results of Shop Floor 1 (25x5x5)

At the highest level of integration, we repeated the experiments five times where at the lower levels of integration we applied only single experiment. Five different random number seeds are used to test the highest level of integration for five replicas. Thus, for four shop floors, 20 experiments are applied, where ES gave the best results in 11 replicas, RS/SA gave the best results in 4 replicas, RS/ES gave best

results in 3 replicas, SA gave best results in one replica and RS gave the best results once. Through the experiments, just once best result are obtained from random search which is totally a random result rather than the power of random search.

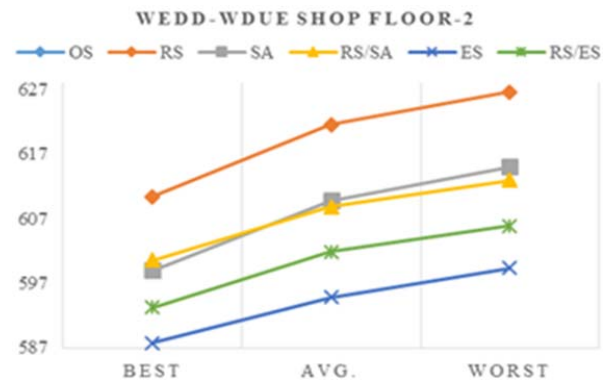


Fig. 3 Results of Shop Floor 2 (75x15x5)

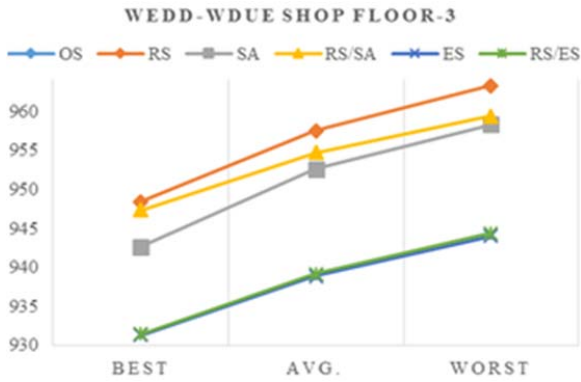


Fig. 4 Results of Shop Floor 3 (125x25x3)

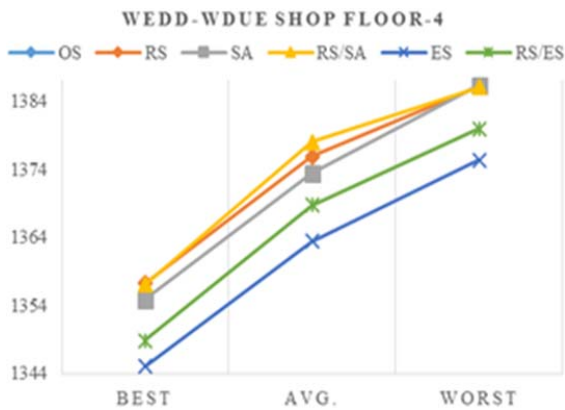


Fig. 5 Results of Shop Floor 4 (175x35x3)

## VII. CONCLUSION

Although there are only a few researches have focused on the IPPSDDA problem, there are numerous works on the sub integrated levels such as IPPS and SWDDA problems. Some of the researches addressing the IPPSDDA problem are mentioned in this research. Since all of these three important manufacturing functions are highly interrelated, it is better to consider them concurrently. A separate solution of the functions causes local optimization for each function, but substantially deteriorates the global optimum. If process plans are prepared separately, then poor and unrealistic process plans can be sent to the downstream scheduling function and this may cause unbalanced machine loading and reduce overall shop floor utilization. Independently performed scheduling function may not consider due date constraint and process plans wisely. Independently given due-dates may be unrealistic for shop floors and unreasonably too close due dates may be designated such that firms may not keep their promise to the customer, and therefore, the reputation of firms may be effected negatively. On the other hand, firms may give unnecessarily long due-dates to customers; this sharply increases, due-date and earliness related costs.

In this study, weighted due date assignment techniques are used where important customers are given closer due dates and are scheduled earlier. As a result, substantial improvements in overall weighted earliness, tardiness and due-date related costs are obtained.

In this research, some pure and hybrid metaheuristics are utilized while solving the integrated problem, namely RS, SA, RS/SA, ES, RS/ES metaheuristics. None of the metaheuristics gave the best results in every experiment. As expected, a fully integrated combination is found as the best integration level. Directed search heuristics are found better than random search. Since random search is marginally attractive only at the beginning of the iterations, combining with other search methods and making hybrid searches was found to be promising metaheuristics.

Initially, the SIRO-RDM combination is tested, and as expected, this lowest level of integration is found to be very poor. At this level process plans are selected independently, due dates are determined randomly and jobs are scheduled in random order. After that, the WEDD-RDM combination is tested; where, scheduling is integrated with process plan selection but due dates are still determined randomly. Substantial improvements are observed at this level of integration.

Later, the SIRO-WDUE combination is tested; where, weighted due-date assignment is integrated with process plan selection, but this time jobs are dispatched in random order. Although weighted due-date assignment provides substantial improvements at the performance function, SIRO dispatching severely deteriorates the penalty function back.

Finally, the WEDD-WDUE combination is tested where the three functions are fully integrated. Process plan selection is integrated with WEDD dispatching and with weighted due date assignment. As expected, this level is found as the best combination and gave the best improvements in the penalty function.

## APPENDIX

### A. Due-Date Assignment Rules

$$\text{WTWK (Weighted Total Work)} \rightarrow \text{Due} = w_1 * k_x * \text{TPT}$$

$$\text{WSLK (Weighted Slack)} \rightarrow \text{Due} = \text{TPT} + w_1 * q_x$$

$$\text{WPPW (Weighted Process Plus Wait)} \rightarrow \text{Due} \\ = q_x * w_1 + w_2 * k_x * \text{TPT}$$

$$\text{WNOP (Weighted Number of operations)} \rightarrow \text{Due} = \text{NOP} * w_1 * k_x$$

$$\text{RDM (Random due date assign.)} \rightarrow \text{Due} \\ = N \sim (3 * P_{avg}, (P_{avg}/2) 2)$$

where;  $w_1, w_2$  are determined according to the weights of the customers; TPT is total processing time;  $P_{avg}$  is mean processing time of all jobs waiting.

### B. Dispatching Rules

WEDD: Weighted Earliest Due Date

SIRO (Service in Random order): A job among waiting jobs is selected randomly to be processed.

## REFERENCES

- [1] H.-C. Zhang and S. Mallur, "An integrated model of process planning

- and production scheduling," *Int. J. Comput. Integr. Manuf.*, vol. 7, no. 6, pp. 356–364, Nov. 1994.
- [2] V. Gordon, J.-M. Proth, and C. Chu, "A survey of the state-of-the-art of common due date assignment and scheduling research," *Eur. J. Oper. Res.*, vol. 139, no. 1, pp. 1–25, May 2002.
- [3] W. Tan and B. Khoshnevis, "Integration of process planning and scheduling—a review," *J. Intell. Manuf.*, vol. 11, no. 1, pp. 51–63, Feb. 2000.
- [4] J. M. Usher, "Evaluating the impact of alternative plans on manufacturing performance," *Comput. Ind. Eng.*, vol. 45, no. 4, pp. 585–596, Dec. 2003.
- [5] K. Bhaskaran, "Process plan selection," *Int. J. Prod. Res.*, vol. 28, no. 8, pp. 1527–1539, 1990.
- [6] N. Morad and A. Zalzal, "Genetic algorithms in integrated process planning and scheduling," *J. Intell. Manuf.*, vol. 10, no. 2, pp. 169–179, Apr. 1999.
- [7] H. Lee and S.-S. Kim, "Integration of Process Planning and Scheduling Using Simulation Based Genetic Algorithms," *Int. J. Adv. Manuf. Technol.*, vol. 18, no. 8, pp. 586–590, Oct. 2001.
- [8] C. Moon, J. Kim, and S. Hur, "Integrated process planning and scheduling with minimizing total tardiness in multi-plants supply chain," *Comput. Ind. Eng.*, vol. 43, no. 1–2, pp. 331–349, Jul. 2002.
- [9] Y. K. Kim, K. Park, and J. Ko, "A symbiotic evolutionary algorithm for the integration of process planning and job shop scheduling," *Comput. Oper. Res.*, vol. 30, no. 8, pp. 1151–1171, Jul. 2003.
- [10] I. Drstvenšek and J. Balič, "Responding ability in dynamic production circumstances," *J. Mater. Process. Technol.*, vol. 133, no. 1, pp. 71–78, 2003.
- [11] C. Moon, Y. H. Lee, C. S. Jeong, and Y. Yun, "Integrated process planning and scheduling in a supply chain," *Comput. Ind. Eng.*, vol. 54, no. 4, pp. 1048–1061, May 2008.
- [12] X. Li, C. Zhang, L. Gao, W. Li, and X. Shao, "An agent-based approach for integrated process planning and scheduling," *Expert Syst. Appl.*, vol. 37, no. 2, pp. 1256–1264, Mar. 2010.
- [13] A. Seker, S. Erol, and R. Botsali, "A neuro-fuzzy model for a new hybrid integrated Process Planning and Scheduling system," *Expert Syst. Appl.*, vol. 40, no. 13, pp. 5341–5351, Oct. 2013.
- [14] L. Zhang and T. N. Wong, "An object-coding genetic algorithm for integrated process planning and scheduling," *Eur. J. Oper. Res.*, vol. 244, no. 2, pp. 434–444, Jul. 2015.
- [15] R. Meenakshi Sundaram and S. Fu, "Process planning and scheduling — A method of integration for productivity improvement," *Comput. Ind. Eng.*, vol. 15, no. 1–4, pp. 296–301, 1988.
- [16] N. Nasr and E. A. Elsayed, "Job shop scheduling with alternative machines," *Int. J. Prod. Res.*, vol. 28, no. 9, pp. 1595–1609, Sep. 1990.
- [17] B. Khoshnevis and Q. M. Chen, "Integration of process planning and scheduling functions," *J. Intell. Manuf.*, vol. 2, no. 3, pp. 165–175, Jun. 1991.
- [18] J. Hutchison, K. Leong, D. Synder, and P. Ward, "Scheduling approaches for random job shop flexible manufacturing systems," *Int. J. Prod. Res.*, vol. 29, no. 5, pp. 1053–1067, May 1991.
- [19] J. M. Usher and K. J. Fernandes, "Dynamic process planning—the static phase," *J. Mater. Process. Technol.*, vol. 61, no. 1–2, pp. 53–58, 1996.
- [20] P. Brandimarte, "Exploiting process plan flexibility in production scheduling: A multi-objective approach," *Eur. J. Oper. Res.*, vol. 114, no. 1, pp. 59–71, Apr. 1999.
- [21] K.-H. Kim and P. J. Egbelu, "Scheduling in a production environment with multiple process plans per job," *Int. J. Prod. Res.*, vol. 37, no. 12, pp. 2725–2753, Aug. 1999.
- [22] A. Weintraub, D. Cormier, T. Hodgson, R. King, J. Willson, and A. Zozom, "Scheduling with alternatives: a link between process planning and scheduling," *IIE Trans.*, vol. 31, no. 11, pp. 1093–1102, Nov. 1999.
- [23] C. Saygin, F. F. Chen, and J. Singh, "Real-time manipulation of alternative routings in flexible manufacturing systems: a simulation study," *Int. J. Adv. Manuf. Technol.*, vol. 18, no. 10, pp. 755–763, 2001.
- [24] C. S. Thomalla, "Job shop scheduling with alternative process plans," *Int. J. Prod. Econ.*, vol. 74, no. 1–3, pp. 125–134, Dec. 2001.
- [25] J. Zhang, L. Gao, F. T. S. Chan, and P. Li, "A holonic architecture of the concurrent integrated process planning system," *J. Mater. Process. Technol.*, vol. 139, no. 1–3, pp. 267–272, Aug. 2003.
- [26] C. Moon and Y. Seo, "Evolutionary algorithm for advanced process planning and scheduling in a multi-plant," *Comput. Ind. Eng.*, vol. 48, no. 2, pp. 311–325, Mar. 2005.
- [27] X. Shao, X. Li, L. Gao, and C. Zhang, "Integration of process planning and scheduling—A modified genetic algorithm-based approach," *Comput. Oper. Res.*, vol. 36, no. 6, pp. 2082–2096, Jun. 2009.
- [28] C. Özgüven, L. Özbakır, and Y. Yavuz, "Mathematical models for job-shop scheduling problems with routing and process plan flexibility," *Appl. Math. Model.*, vol. 34, no. 6, pp. 1539–1548, Jun. 2010.
- [29] X. Li, X. Shao, L. Gao, and W. Qian, "An effective hybrid algorithm for integrated process planning and scheduling," *Int. J. Prod. Econ.*, vol. 126, no. 2, pp. 289–298, Aug. 2010.
- [30] R. K. Phanden, A. Jain, and R. Verma, "Integration of process planning and scheduling: a state-of-the-art review," *Int. J. Comput. Integr. Manuf.*, vol. 24, no. 6, pp. 517–534, Jun. 2011.
- [31] X. Li, L. Gao, and W. Li, "Application of game theory based hybrid algorithm for multi-objective integrated process planning and scheduling," *Expert Syst. Appl.*, vol. 39, no. 1, pp. 288–297, Jan. 2012.
- [32] C. Dewu, S. Li, and T. Guochun, "Single machine scheduling with common due date assignment in a group technology environment," *Math. Comput. Model.*, vol. 25, no. 3, pp. 81–90, 1997.
- [33] M. Y. Kovalyov, "Batch scheduling and common due date assignment problem: an NP-hard case," *Discrete Appl. Math.*, vol. 80, no. 2–3, pp. 251–254, 1997.
- [34] D. Biskup and H. Jahnke, "Common due date assignment for scheduling on a single machine with jointly reducible processing times," *Int. J. Prod. Econ.*, vol. 69, no. 3, pp. 317–322, Feb. 2001.
- [35] T. C. E. Cheng, Z.-L. Chen, and N. V. Shakhlevich, "Common due date assignment and scheduling with ready times," *Comput. Oper. Res.*, vol. 29, no. 14, pp. 1957–1967, Dec. 2002.
- [36] V. Lauff and F. Werner, "Scheduling with common due date, earliness and tardiness penalties for multimachine problems: A survey," *Math. Comput. Model.*, vol. 40, no. 5–6, pp. 637–655, Sep. 2004.
- [37] V. S. Gordon and V. A. Strusevich, "Single machine scheduling and due date assignment with positionally dependent processing times," *Eur. J. Oper. Res.*, vol. 198, no. 1, pp. 57–62, Oct. 2009.
- [38] H. Allaoua and I. Osmane, "Variable Parameters Lengths Genetic Algorithm for Minimizing Earliness-Tardiness Penalties of Single Machine Scheduling With a Common Due Date," *Electron. Notes Discrete Math.*, vol. 36, pp. 471–478, Aug. 2010.
- [39] N. H. Tuong and A. Soukhal, "Due dates assignment and JIT scheduling with equal-size jobs," *Eur. J. Oper. Res.*, vol. 205, no. 2, pp. 280–289, Sep. 2010.
- [40] Y. Yin, T. C. E. Cheng, D. Xu, and C.-C. Wu, "Common due date assignment and scheduling with a rate-modifying activity to minimize the due date, earliness, tardiness, holding, and batch delivery cost," *Comput. Ind. Eng.*, vol. 63, no. 1, pp. 223–234, Aug. 2012.
- [41] H. Luss and M. B. Rosenwein, "A due date assignment algorithm for multiproduct manufacturing facilities," *Eur. J. Oper. Res.*, vol. 65, no. 2, pp. 187–198, 1993.
- [42] V. Gordon and W. Kubiak, "Single machine scheduling with release and due date assignment to minimize the weighted number of late jobs," *Inf. Process. Lett.*, vol. 68, no. 3, pp. 153–159, Nov. 1998.
- [43] T. C. E. Cheng and M. Y. Kovalyov, "Complexity of parallel machine scheduling with processing-plus-wait due dates to minimize maximum absolute lateness," *Eur. J. Oper. Res.*, vol. 114, no. 2, pp. 403–410, Apr. 1999.
- [44] V. S. Gordon and V. A. Strusevich, "Earliness penalties on a single machine subject to precedence constraints: SLK due date assignment," *Comput. Oper. Res.*, vol. 26, no. 2, pp. 157–177, 1999.
- [45] A. Baykasoğlu, M. Göçken, and Z. D. Unutmaz, "New approaches to due date assignment in job shops," *Eur. J. Oper. Res.*, vol. 187, no. 1, pp. 31–45, May 2008.
- [46] Y. Xia, B. Chen, and J. Yue, "Job sequencing and due date assignment in a single machine shop with uncertain processing times," *Eur. J. Oper. Res.*, vol. 184, no. 1, pp. 63–75, Jan. 2008.
- [47] S. Li, C. T. Ng, and J. Yuan, "Scheduling deteriorating jobs with CON/SLK due date assignment on a single machine," *Int. J. Prod. Econ.*, vol. 131, no. 2, pp. 747–751, Jun. 2011.
- [48] X. Cai, V. Y. S. Lum, and J. M. T. Chan, "Scheduling about a common due date with kob-dependent asymmetric earliness and tardiness penalties," *Eur. J. Oper. Res.*, vol. 98, no. 1, pp. 154–168, 1997.
- [49] M. Birman and G. Mosheiov, "A note on a due-date assignment on a two-machine flow-shop," *Comput. Oper. Res.*, vol. 31, no. 3, pp. 473–480, Mar. 2004.
- [50] L. Min and W. Cheng, "Genetic algorithms for the optimal common due date assignment and the optimal scheduling policy in parallel machine earliness/tardiness scheduling problems," *Robot. Comput. Integr. Manuf.*, vol. 22, no. 4, pp. 279–287, Aug. 2006.
- [51] T. Yang, Z. He, and K. K. Cho, "An effective heuristic method for

generalized job shop scheduling with due dates," *Comput. Ind. Eng.*, vol. 26, no. 4, pp. 647–660, 1994.

- [52] V. Vinod and R. Sridharan, "Simulation modeling and analysis of due-date assignment methods and scheduling decision rules in a dynamic job shop production system," *Int. J. Prod. Econ.*, vol. 129, no. 1, pp. 127–146, Jan. 2011.
- [53] S. R. Lawrence, "Negotiating due-dates between customers and producers," *Int. J. Prod. Econ.*, vol. 37, no. 1, pp. 127–138, 1994.
- [54] J. N. Gupta, K. Krüger, V. Lauff, F. Werner, and Y. N. Sotskov, "Heuristics for hybrid flow shops with controllable processing times and assignable due dates," *Comput. Oper. Res.*, vol. 29, no. 10, pp. 1417–1439, 2002.
- [55] H. I. Demir, T. Cakar, M. Ipek, O. Uygun, and M. Sari, "Process Planning and Due-date Assignment with ATC Dispatching where Earliness, Tardiness and Due-dates are Punished," *J. Ind. Intell. Inf. Vol.*, vol. 3, no. 3, 2015.
- [56] H. I. Demir and H. Taskin, "Integrated Process Planning, Scheduling and Due-Date Assignment," PhD Thesis, Sakarya University, 2005.
- [57] E. Ceven and H. I. Demir, "Benefits of Integrating Due-Date Assignment with Process Planning and Scheduling," Master of Science Thesis, Sakarya University, 2007.
- [58] H. I. Demir, O. Uygun, I. Cil, M. Ipek, and M. Sari, "Process Planning and Scheduling with SLK Due-Date Assignment where Earliness, Tardiness and Due-Dates are Punished," *J. Ind. Intell. Inf. Vol.*, vol. 3, no. 3, 2015.
- [59] S. Kirkpatrick, C. D. Gelatt, M. P. Vecchi, and others, "Optimization by simulated annealing," *science*, vol. 220, no. 4598, pp. 671–680, 1983.
- [60] I. Rechenberg, *Cybernetic solution path of an experimental problem*, vol. 1122. Ministry of Aviation, Royal Aircraft Establishment, Library Translation, 1965.
- [61] H.-P. Schwefel, *Numerical optimization of computer models*. John Wiley & Sons, Inc., 1981.



**Özer Uygun** was born in Sakarya, Turkey in 1976. In 1994 he entered Sakarya University, Turkey to Industrial Engineering Department. He got his Bachelor of Science degree in Industrial Engineering in 1999. He got Master of Science degree in 2002 and Ph.D. degree in 2008 in Industrial Engineering from Sakarya University, Turkey.

He started to his academic position at Marmara University and worked as a Lecturer between 2000 and 2003. Then he worked as a Research Assistant at Sakarya University between 2003 and 2008 and now works as an Assistant Professor at this university. He was a researcher in EU FP6 Network of Excellence (I\*PROMS: 2004-2009) and FP6 STREP Project (IWARD: 2007-2009). He has successfully completed the EFQM Assessor Training in Brussels in 2015. His research areas are Multi-criteria decision making, Fuzzy decision making, and Application of Operation research.



**Halil Ibrahim Demir** was born in Sivas, Turkey in 1971.

In 1988 he received a full scholarship and entered Bilkent University, Ankara, Turkey, to study in the Industrial Engineering Department. He got his Bachelor of Science degree in Industrial Engineering in 1993. He then went to Germany for graduate study, studying German at the intermediate level. In 1994, he received a full scholarship for graduate study in the USA from the Ministry of Education of Turkey. In 1997 he received a Master of Science degree in Industrial Engineering from Lehigh University, Bethlehem, Pennsylvania, USA. He was then accepted to Northeastern University, Boston, Massachusetts for Ph.D. study. He finished his Ph.D. courses at Northeastern and completed a Ph.D. thesis at Sakarya University, Turkey in 2005 for a Ph.D. in Industrial Engineering. He obtained an academic position at Sakarya University as an Assistant Professor. His Research Areas are Production Planning, Scheduling, Application of OR, Simulation, Artificial intelligence techniques, Genetic algorithms, Artificial Neural Networks, Fuzzy Logic and Decision making.



**Abdullah Hulusi Koccam** was born in Elazig, Turkey in 1987.

He received the BSc degree from Selcuk University Industrial Engineering Department in 2008, and MSc degree from Sakarya University Industrial Engineering Department in 2010. He is currently PhD student and research assistant of Industrial Engineering at the Sakarya University. He has been worked on fuzzy project scheduling with meta-heuristic methods in his MSc thesis. His research area of interests are project scheduling, meta-heuristic methods, optimization, test measurement, and school performance.



**Fuat Simsir** was born in Kocaeli, Turkey in 1976.

In 1995 he entered Sakarya University, Turkey to Industrial Engineering Department. He got his Bachelor of Science degree in Industrial Engineering in 1999. He got Master of Science degree in 2002 and Ph.D. degree in 2008 in Industrial Engineering from Sakarya University, Turkey. He started to his academic position as a Research Assistant at Sakarya University between 2003 and 2008 and now works as an Assistant Professor at Karabuk University. His research areas are Genetic Algorithms, Fuzzy Logic, Scheduling, Simulation Operations Research, and Production Planning