

Capacitance Models of AlGaIn/GaN High Electron Mobility Transistors

A. Douara, N. Kermas, B. Djellouli

Abstract—In this study, we report calculations of gate capacitance of AlGaIn/GaN HEMTs with nextnano device simulation software. We have used a physical gate capacitance model for III-V FETs that incorporates quantum capacitance and centroid capacitance in the channel. These simulations explore various device structures with different values of barrier thickness and channel thickness. A detailed understanding of the impact of gate capacitance in HEMTs will allow us to determine their role in future 10 nm physical gate length node.

Keywords—AlGaIn/GaN, centroid capacitance, gate capacitance, HEMT, quantum capacitance.

I. INTRODUCTION

High performance power electronic transistors require high breakdown voltage, low specific on-state resistance, high current rating, potential for high temperature operation, low leakage and fast switching time. III-V group semiconductors, such as GaN, are important candidates in developing the third generation semiconductor devices of high temperature, high frequency and high power density due to their excellent intrinsic physical properties, such as wide bandgap, high breakdown electric field, large electron saturation velocity, and good thermal conductivity. Presently, the progress in the development of GaN-based power devices is accelerating rapidly [1].

The epitaxial growth of AlGaIn over GaN leads to the formation of two-dimensional electron gas 2DEG at the heterointerface, with 2DEG density of the order of 10^{13} cm^{-2} . In case of doped barrier, the electrons present in the AlGaIn layer fall in the quantum well and become a part of the 2DEG density [3], [5]. The growth of AlGaIn over GaN epi-layer undergoes spontaneous and piezoelectric polarizations which results into a very thin layer of electrons in the heterointerface called 2DEG [2], [4].

II. GATE CAPACITANCE IN III-V HEMT

The gate capacitance of a III-V FET in strong inversion can be modeled as the series combination of the barrier capacitance (C_B) and the inversion-layer capacitance (C_{inv}) (Fig. 1) [6]. This one consists, in turn, of a parallel combination of the contributions of each occupied electron

subband in the channel. In Fig. 1, C_{inv1} and C_{inv2} indicate the 1st subband and 2nd subband inversion-layer capacitances, respectively.

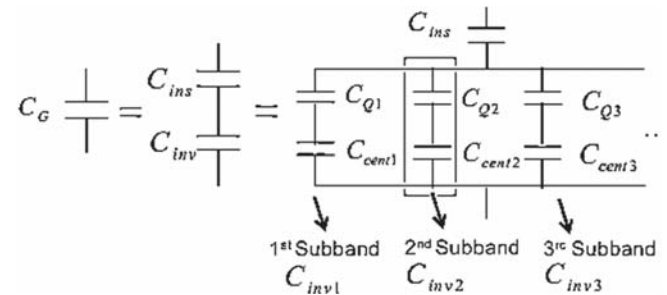


Fig. 1 Equivalent circuit diagram of gate capacitance in a III-V FET

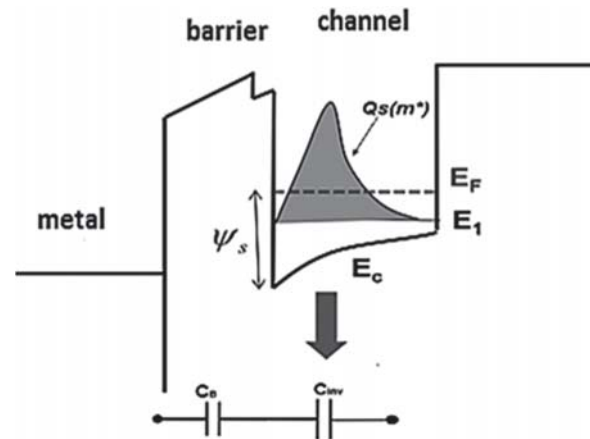


Fig. 2 Conduction band diagram of a III-V FET in strong inversion

For each subband i , the inversion-layer capacitance (C_{inv}) consists of the quantum capacitance (C_{Q-i}) and the centroid capacitance (C_{cent-i}) which are connected in series. This can be obtained from the definition of inversion-layer capacitance:

$$C_{inv} = \frac{\partial(-Q_s)}{\partial\psi_s} = \frac{q\partial(-Q_s)}{\partial(E_F - E_c)}$$

where ψ_s is the surface potential, and E_c is the conduction band edge at the barrier channel interface on the channel side. Q_s is the total electron charge in the channel. That can be easily computed through:

$$Q_s = \sum_i Q_i = \sum_i \int_{E_i}^{\infty} \frac{\frac{m^*q}{\pi\hbar^2}}{1 + \exp\left(\frac{E - E_F}{KT}\right)}$$

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where Q_i is the electron charge of subband i in the channel, E_i is the energy level of subband i , and m_{\parallel}^* is the in-plane effective mass of the channel material.

We can in general write:

$$\left(\frac{q\partial(-Q_i)}{\partial(E_F - E_c)}\right)^{-1} = \left(\frac{q\partial(-Q_i)}{\partial(E_F - E_i)}\right)^{-1} + \left(\frac{q\partial(-Q_i)}{\partial(E_i - E_c)}\right)^{-1}$$

We can then define C_{Q_i} as:

$$C_{Q_i} = \frac{q\partial(-Q_i)}{\partial(E_F - E_i)}$$

C_{Q_i} is the quantum capacitance of subband i and corresponds to the derivative of electron charge in subband i with respect to the energy difference between E_F and E_i .

We also define C_{cent_i} as:

$$C_{cent_i} = \frac{q\partial(-Q_i)}{\partial(E_i - E_c)}$$

C_{cent_i} is the centroid capacitance of subband i and corresponds to the derivative of electron charge in subband i with respect to the energy difference between E_i and E_c . Then, C_{inv} can be expressed as

$$C_{inv} = \sum_i \left(\frac{1}{C_{Q_i}} + \frac{1}{C_{cent_i}} \right)^{-1}$$

We can derive additional formulas for C_{Q_i} and C_{cent_i} :

$$C_{Q_i} = \frac{q\partial(-Q_i)}{\partial(E_F - E_i)}$$

$$\frac{q\partial\left(-\int_{E_i}^{\infty} \frac{\frac{m_{\parallel}^*}{\pi\hbar^2}}{1 + \exp\left(\frac{E - E_F}{KT}\right)} dE\right)}{\partial(E_F - E_i)} = \frac{\frac{m_{\parallel}^* q^2}{\pi\hbar^2}}{1 + \exp\left(\frac{E_i - E_F}{KT}\right)}$$

$$C_{cent_i} = \frac{q\partial(-Q_i)}{\partial(E_i - E_c)} = C_{Q_i} \cdot \frac{\partial(E_F - E_i)}{\partial(E_i - E_c)}$$

The barrier capacitance is simply the permittivity (ϵ) over the barrier thickness (d_B):

$$C_B = \frac{\epsilon}{d_B}$$

III. HEMT SIMULATION STRUCTURE

One-dimensional numerical simulations of the AlGaIn/GaN HEMT were performed using the structure illustrated in Fig. 3. The structure is constituted of an AlGaIn barrier layer and a GaN channel layer. The barrier contains a δ -doping layer that defines the concentration of electrons in the channel. The gate is T-shaped. L_G is the gate length, d_c is the width of the GaN channel, and d_B is the barrier thickness.

IV. RESULTS

A. Energy Band Structure

Our physical model requires correct values of E_i and E_c with respect to E_F . These physical values can be calculated from a self-consistent solution of the 1-D Poisson and Schrodinger equations, which respectively, are:

$$\nabla\epsilon\nabla V = -\rho$$

$$\left(\frac{-\hbar^2}{2} \nabla \frac{1}{m^*} \nabla + \Delta E_c - qV\right) \varphi_i = E_i \varphi_i$$

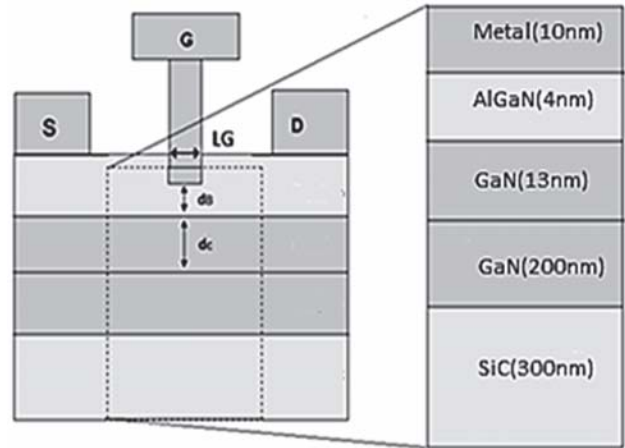


Fig. 3 Sketch of simulation d'un HEMT AlGaIn/GaN

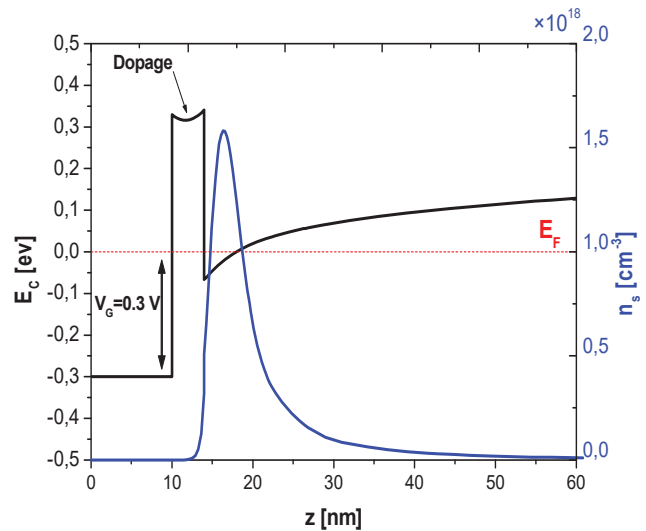


Fig. 4 Conduction band (E_c) and electrons density ($n_s(z)$).

Fig. 4 shows the conduction band profile and the electron density profile along the vertical axis of the HEMT structure for a gate voltage $V_G = 0.3$ Volt. The Schottky barrier height between the metal gate and cap layer is taken equal to 0.63 eV. We calculate the electrons density and the conduction band energy at different gate voltages. Subband energy levels and conduction band edge profiles are collected to generate our physical model.

B. Sheet Carrier Concentration (N_s) vs Gate Voltage (V_G)

The first step in our calculations serves as an initial validation of our model by simulating several structures with different doping concentrations with the Nextnano solver and comparing its predictions with those of the analytical model. Fig. 5 shows results of the analytical modeling of sheet carrier concentration as a function of the gate voltage V_G along with those obtained from numerical simulations with the Nextnano solver. The analytical model shows excellent agreement with the numerical simulation.

The effect of varying the molar fraction x of the ternary compound $Al_xGa_{1-x}N$ on the gate capacitance is shown in Fig. 6. The variation of the total electron density as a function of the gate voltage V_G for different values of the mole fraction x ranging from 0.2 to 0.7 is calculated with the nextnano simulator and shown in solid lines, while the symbols show the sheet charge density calculated with the analytical model. We can see the total agreement between the two approaches. We can also see in Fig. 6 that the carrier density increases with the augmentation of the molar fraction of Aluminum content.

combination of quantum capacitance, centroid capacitance, and insulator capacitance. The analytical model results agree very well with numerical calculation results shown in solid lines in all three cases.

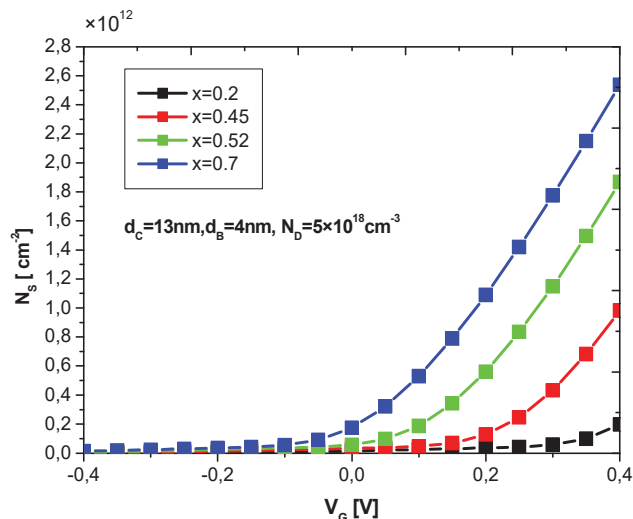


Fig. 6 Electron concentration (N_s) as a function of the gate voltage (V_G) for different values of molar fraction(x)

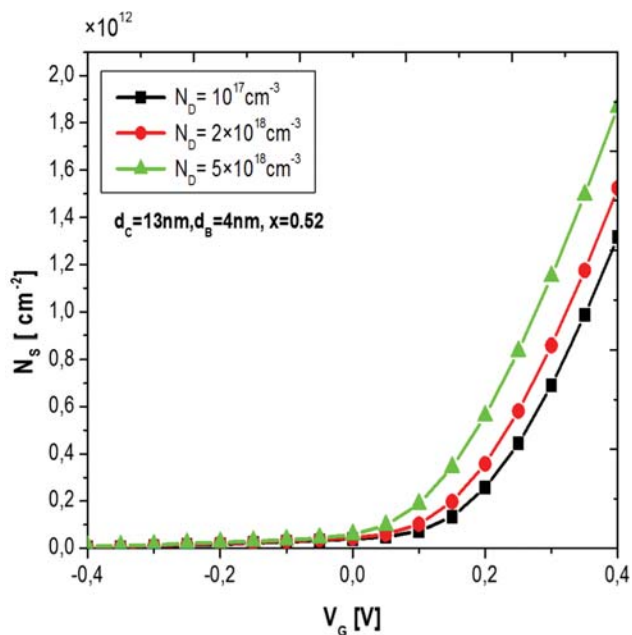


Fig. 5 Electrons concentration (N_s) as a function of voltage (V_G) for different values of doping concentration (N_D). Solid line (Nextnano), Symbols (analytical expression)

In Fig. 7, we show the variation of the total electron density as a function of the gate voltage V_G for different values of barrier thickness d_B . We observe the agreement between results of the analytical model and those of the Nextnano solver. We observe an increasing of the carrier charge density with the increasing of the barrier thickness. We can relate this to the augmentation of carrier density of electrons flowing from the barrier to the channel.

The gate capacitance as a function of gate voltage is shown in Fig. 8. It is calculated for different barrier thicknesses. The symbols indicate the gate capacitance calculated from the

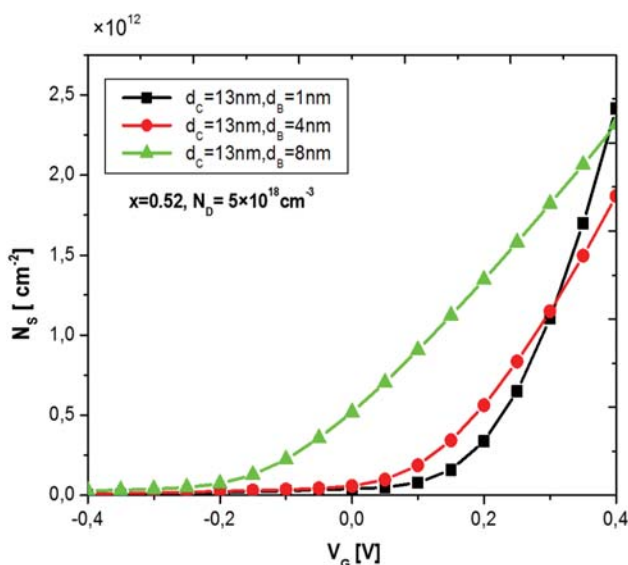


Fig. 7 Electron concentration (N_s) as a function of the gate voltage (V_G) for different values of the barrier thickness (d_B)

In Fig. 9 we show the decomposition of the total gate capacitance into each gate capacitance component. This allows us to determine the influence of component and also to compare them with each other. From Fig. 9, we can understand the relative roles of the quantum capacitance and the centroid capacitance in this device.

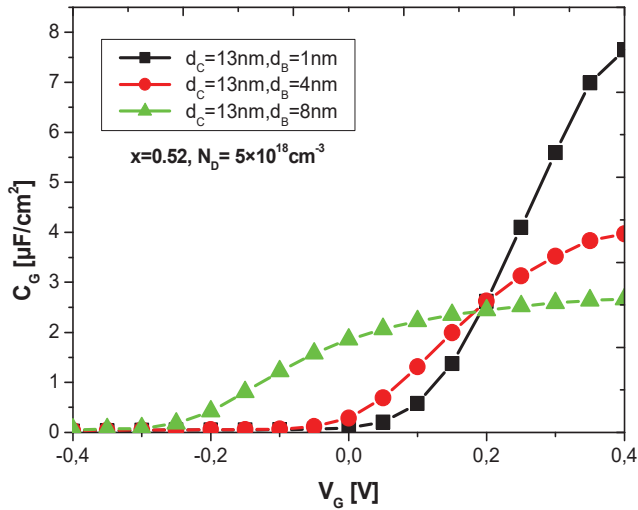


Fig. 8 Gate capacitance (C_G) as a function of the gate voltage (V_G) for different values of the barrier thickness (d_B)

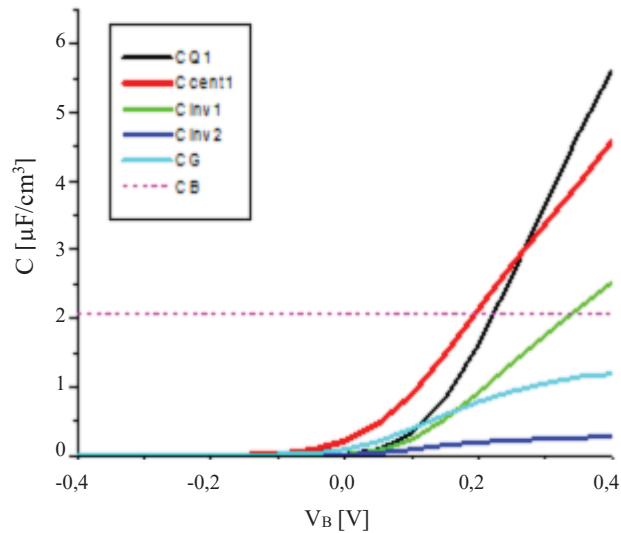


Fig. 9 Gate capacitance (C_G) and Barrier insulator capacitance (C_B), inversion-layer capacitance (C_{inv}) for the 1st and 2nd subband, Centroid capacitance (C_{cent1}) and quantum capacitance (C_{Q1}) for the 1st subband

V.CONCLUSION

The results of simulations presented in this article show the effect of some physical and geometrical parameters of the electron density. We have shown that it is possible to decompose the gate capacitance in quantum and centroid capacitance. The small inversion-layer capacitance mostly results from a relatively small centroid capacitance. In addition, we also found that the 1st subband dominates the overall gate capacitance in the operational range of scaled down HEMTs.

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