

## Standardized Description and Modeling Methods of Semiconductor IP Interfaces

**Authors :** Seongsoo Lee

**Abstract :** IP reuse is an effective design methodology for modern SoC design to reduce effort and time. However, description and modeling methods of IP interfaces are different due to different IP designers. In this paper, standardized description and modeling methods of IP interfaces are proposed. It consists of 11 items such as IP information, model provision, data type, description level, interface information, port information, signal information, protocol information, modeling level, modeling information, and source file. The proposed description and modeling methods enables easy understanding, simulation, verification, and modification in IP reuse.

**Keywords :** interface, standardization, description, modeling, semiconductor IP

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